

ELECTRICAL SPECIFICATIONS:

1.0 INDUCTANCE: (P1-J1) : (P3-J3) : ( P4-J4) : (P7-J7) : 65 uH TYP @.05V, 10KHz

2.0 DC RESISTANCE: P8-J8 = P7-J7 : .5 OHM MAX  
 P6-J6 = P3-J3 : .5 OHM MAX  
 P5-J5 = P4-J4 : .5 OHM MAX  
 P2-J2 = P1-J1 : .5 OHM MAX

3.0 COMMON MODE:

FREQUENCY (MHz)	CMA (-dB)	
	TYPICAL	MIN
1	8	5
5	17	15
20	23	15
70	25	20
200	22	17
500	11	6

Bel Stewart Connector  
 11118 Susquehanna Trail, South  
 Glen Rock, Pa 17327-9199  
 717.234.7512

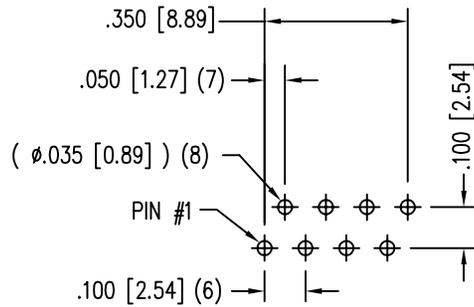
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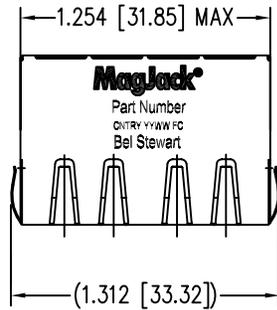
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SHEET  
1 OF 3

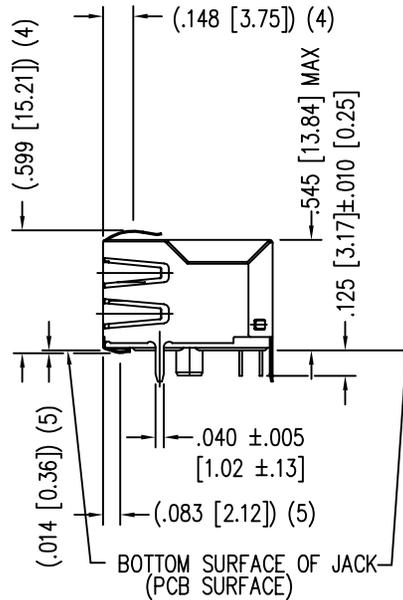
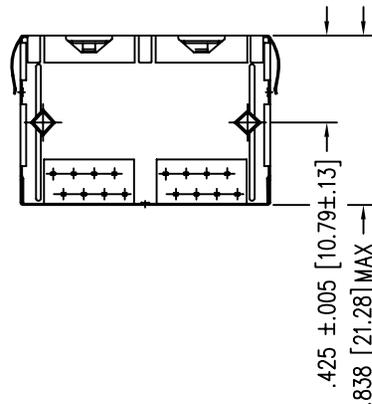
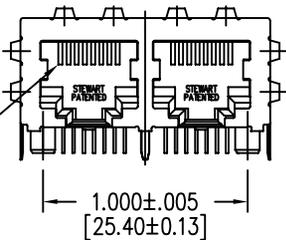
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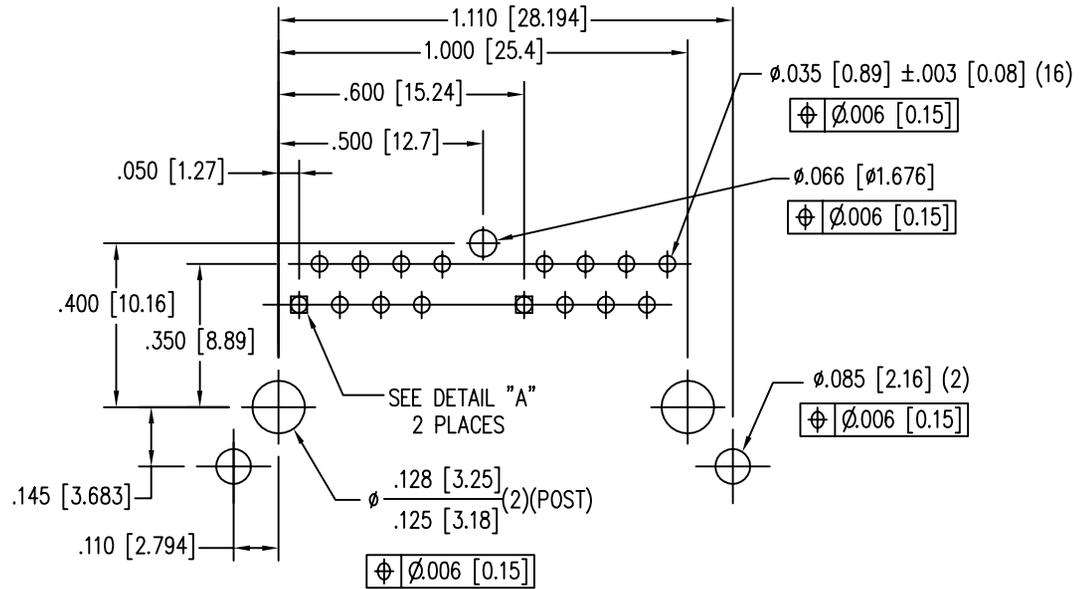
DETAIL "A"  
TYPICAL HOLE LAYOUT  
2 PLACES



PIN: J1



BOTTOM SURFACE OF JACK  
(PCB SURFACE)



P.C.B. RECOMMENDED HOLE LAYOUT  
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.

NOTES:

- CONNECTOR MATERIALS:  
HOUSING: THERMOPLASTIC UL94 V-0  
CONTACT/SHIELD: COPPER ALLOY  
SHIELD PLATING: NICKEL OR TIN  
CONTACT PLATING: SELECTIVE GOLD,  
50 MICRO-INCHES MIN. IN CONTACT AREA.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.  
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- WAVE SOLDER COMPATIBLE - PREHEAT 125°C/90SECS.

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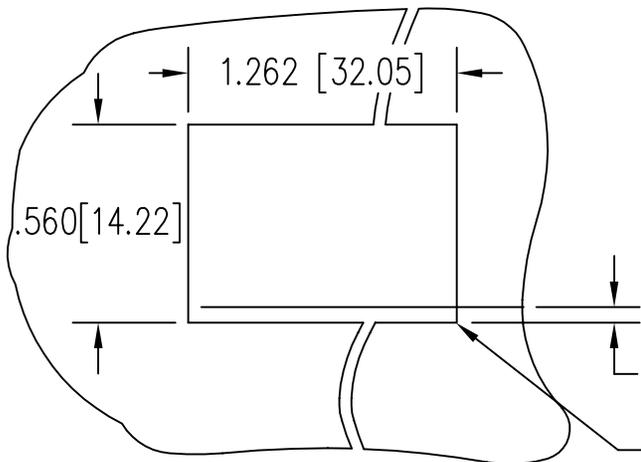
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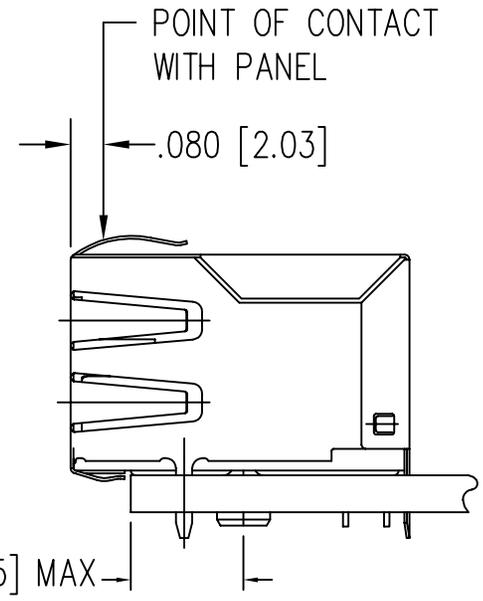
SHEET  
2 OF 3

DRAWING NO. SI-20161 REV. 07



SUGGESTED PANEL OPENING

.000 (TOP OF PCB TO BOTTOM OF OPENING)  
 .010 [.254] MAX. RADIUS(4)



.275 [6.985] MAX

1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE  $\pm 0.005$  [0.13]

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