

**EV32C6B5A1-26.000M TR** [Click part number to visit Part Number Details page](#)
**REGULATORY COMPLIANCE** (Data Sheet downloaded on Nov 30, 2017)

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**ITEM DESCRIPTION**

Voltage Controlled Quartz Crystal Clock Oscillators VCXO LVCMOS/TTL (CMOS) 3.3Vdc 6 Pad 5.0mm x 7.0mm Ceramic Surface Mount (SMD) 26.000MHz ±50ppm Maximum -40°C to +85°C ±100ppm Minimum 10% Typical, 20% Maximum

**ELECTRICAL SPECIFICATIONS**

<b>Nominal Frequency</b>	26.000MHz
<b>Frequency Tolerance/Stability</b>	±50ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, Shock, and Vibration.)
<b>Aging at 25°C</b>	±2ppm/First Year Typical, ±10ppm/10 Years Maximum
<b>Operating Temperature Range</b>	-40°C to +85°C
<b>Supply Voltage</b>	3.3Vdc ±10%
<b>Input Current</b>	15mA Maximum
<b>Output Voltage Logic High (Voh)</b>	90% of Vdd Minimum (IOH = -4mA)
<b>Output Voltage Logic Low (Vol)</b>	10% of Vdd Maximum (IOL = +4mA)
<b>Rise/Fall Time</b>	5nSec Maximum (Measured at 20% to 80% of Waveform)
<b>Duty Cycle</b>	50 ±10(%) (Measured at 50% of Waveform)
<b>Load Drive Capability</b>	15pF LVCMOS Load Maximum
<b>Output Logic Type</b>	CMOS
<b>Absolute Pull Range</b>	±100ppm Minimum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, Shock, Vibration, and Aging over the Control Voltage (Vc).)
<b>Control Voltage</b>	0.3Vdc to 3.0Vdc (Test Condition for APR)
<b>Control Voltage Range</b>	0.0Vdc to Vdd
<b>Linearity</b>	10% Typical, 20% Maximum
<b>Transfer Function</b>	Positive Transfer Characteristic
<b>Modulation Bandwidth</b>	10kHz Minimum (Measured at -3dB, Vc = 1.65Vdc)
<b>Input Impedance</b>	50kOhms Minimum
<b>Input Leakage Current</b>	10µA Maximum
<b>Phase Noise</b>	All Values are Typical -70dBc/Hz at offset of 10Hz -100dBc/Hz at offset of 100Hz -130dBc/Hz at offset of 1kHz -147dBc/Hz at offset of 10kHz -152dBc/Hz at offset of 100kHz -155dBc/Hz at offset of 1MHz
<b>Tri-State Input Voltage (Vih and Vil)</b>	90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)
<b>RMS Phase Jitter</b>	1pSec Maximum (Fj = 12kHz to 20MHz; Random)
<b>Start Up Time</b>	10mSec Maximum
<b>Storage Temperature Range</b>	-55°C to +125°C

**ENVIRONMENTAL & MECHANICAL SPECIFICATIONS**

<b>ESD Susceptibility</b>	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
<b>Fine Leak Test</b>	MIL-STD-883, Method 1014, Condition A
<b>Flammability</b>	UL94-V0
<b>Gross Leak Test</b>	MIL-STD-883, Method 1014, Condition C
<b>Mechanical Shock</b>	MIL-STD-883, Method 2002, Condition B

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<b>Moisture Resistance</b>	MIL-STD-883, Method 1004
<b>Moisture Sensitivity</b>	J-STD-020, MSL 1
<b>Resistance to Soldering Heat</b>	MIL-STD-202, Method 210, Condition K
<b>Resistance to Solvents</b>	MIL-STD-202, Method 215
<b>Solderability</b>	MIL-STD-883, Method 2003
<b>Temperature Cycling</b>	MIL-STD-883, Method 1010, Condition B
<b>Vibration</b>	MIL-STD-883, Method 2007, Condition A

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### MECHANICAL DIMENSIONS (all dimensions in millimeters)



PIN	CONNECTION
1	Control Voltage
2	Tri-State
3	Case Ground
4	Output
5	No Connect
6	Supply Voltage

LINE	MARKING
1	ECLIPTEK
2	26.000M
3	XXXXX XXXXX=Ecliptek Manufacturing Identifier

### Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are  $\pm 0.1$

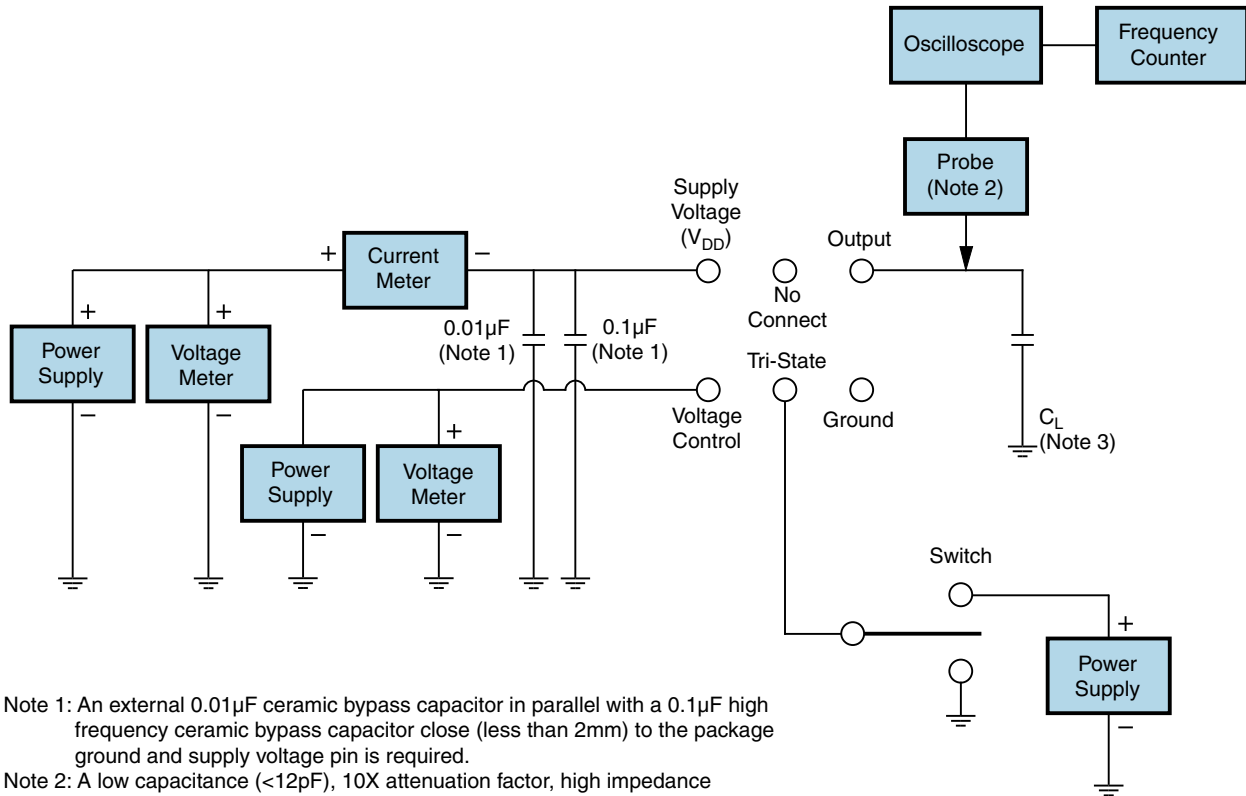
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**OUTPUT WAVEFORM & TIMING DIAGRAM**



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## Test Circuit for CMOS Output



Note 1: An external  $0.01\mu\text{F}$  ceramic bypass capacitor in parallel with a  $0.1\mu\text{F}$  high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance ( $<12\text{pF}$ ), 10X attenuation factor, high impedance ( $>10\text{Mohms}$ ), and high bandwidth ( $>300\text{MHz}$ ) passive probe is recommended.

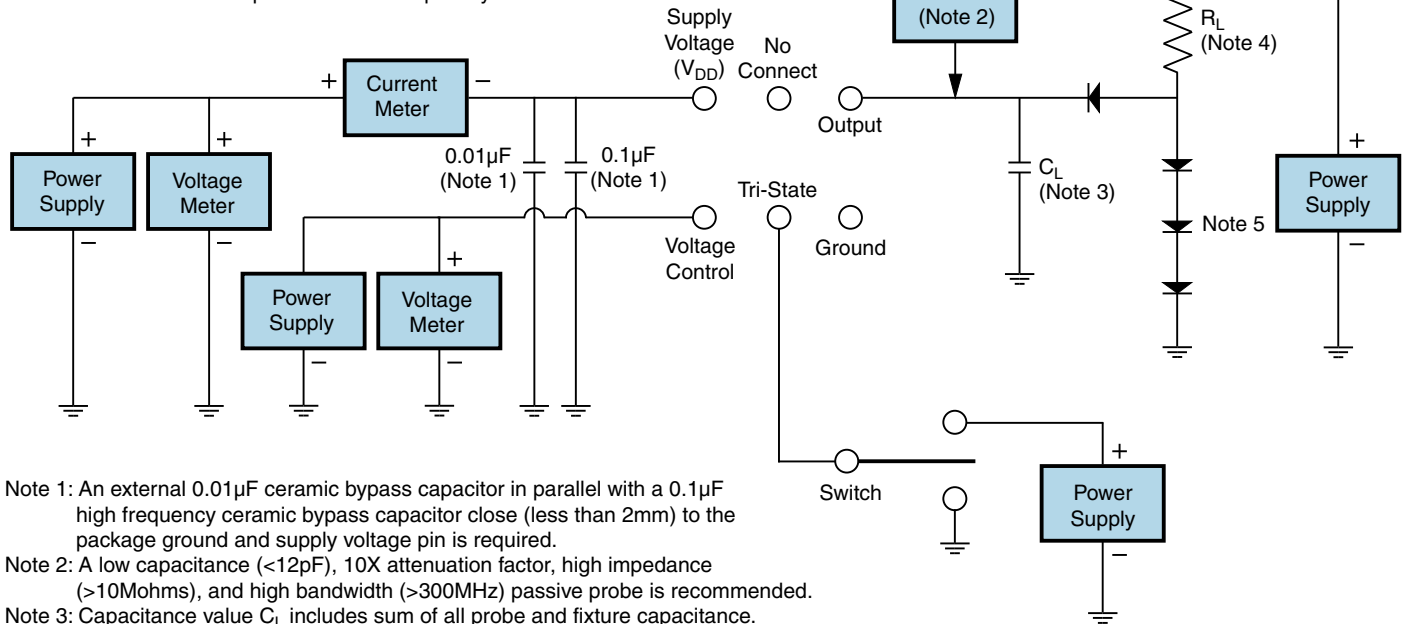
Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

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## Test Circuit for TTL Output

Output Load Drive Capability	$R_L$ Value (Ohms)	$C_L$ Value (pF)
10TTL	390	15

Table 1:  $R_L$  Resistance Value and  $C_L$  Capacitance Value Vs. Output Load Drive Capability



Note 1: An external  $0.01\mu\text{F}$  ceramic bypass capacitor in parallel with a  $0.1\mu\text{F}$  high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance ( $<12\text{pF}$ ), 10X attenuation factor, high impedance ( $>10\text{Mohms}$ ), and high bandwidth ( $>300\text{MHz}$ ) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

Note 4: Resistance value  $R_L$  is shown in Table 1. See applicable specification sheet for 'Load Drive Capability'.

Note 5: All diodes are MMBD7000, MMBD914, or equivalent.



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## Recommended Solder Reflow Methods

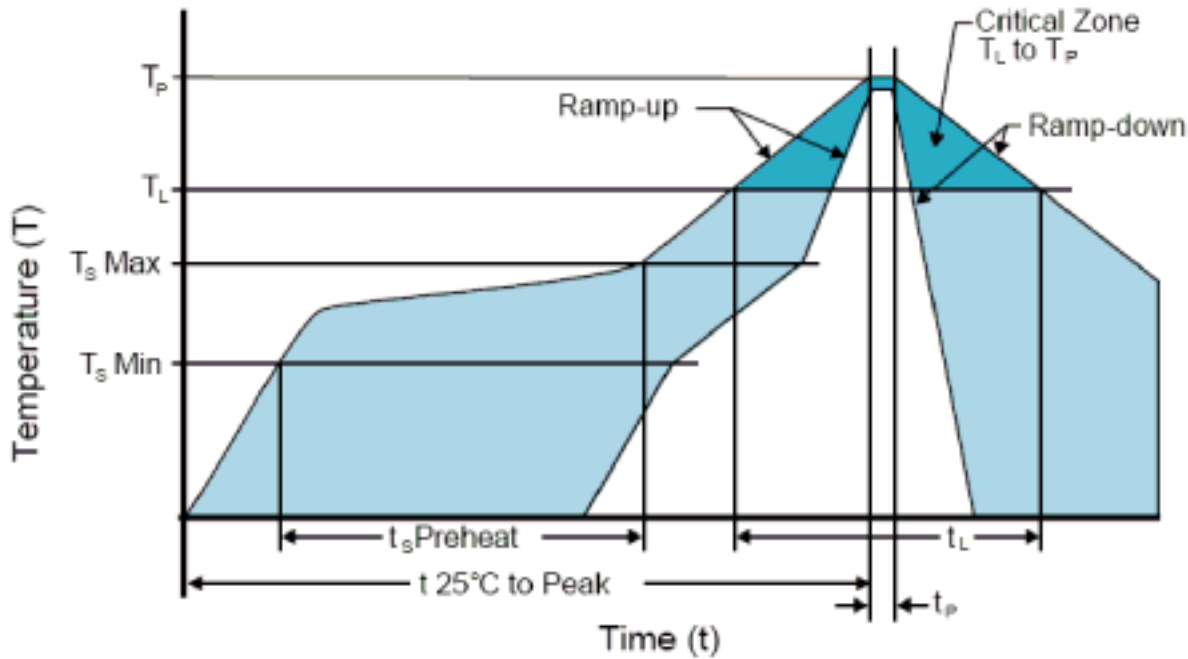


### High Temperature Infrared/Convection

<b><math>T_S \text{ MAX}</math> to <math>T_L</math> (Ramp-up Rate)</b>	3°C/Second Maximum
<b>Preheat</b>	
- Temperature Minimum ( $T_S \text{ MIN}$ )	150°C
- Temperature Typical ( $T_S \text{ TYP}$ )	175°C
- Temperature Maximum ( $T_S \text{ MAX}$ )	200°C
- Time ( $t_s \text{ MIN}$ )	60 - 180 Seconds
<b>Ramp-up Rate (<math>T_L</math> to <math>T_P</math>)</b>	3°C/Second Maximum
<b>Time Maintained Above:</b>	
- Temperature ( $T_L$ )	217°C
- Time ( $t_L$ )	60 - 150 Seconds
<b>Peak Temperature (<math>T_P</math>)</b>	260°C Maximum for 10 Seconds Maximum
<b>Target Peak Temperature (<math>T_P \text{ Target}</math>)</b>	250°C +0/-5°C
<b>Time within 5°C of actual peak (<math>t_p</math>)</b>	20 - 40 Seconds
<b>Ramp-down Rate</b>	6°C/Second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	8 Minutes Maximum
<b>Moisture Sensitivity Level</b>	Level 1

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## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

Ts MAX to Tl (Ramp-up Rate)	5°C/Second Maximum
<b>Preheat</b>	
- Temperature Minimum (Ts MIN)	N/A
- Temperature Typical (Ts TYP)	150°C
- Temperature Maximum (Ts MAX)	N/A
- Time (ts MIN)	60 - 120 Seconds
<b>Ramp-up Rate (Tl to Tp)</b>	5°C/Second Maximum
<b>Time Maintained Above:</b>	
- Temperature (Tl)	150°C
- Time (tL)	200 Seconds Maximum
<b>Peak Temperature (Tp)</b>	240°C Maximum
<b>Target Peak Temperature (Tp Target)</b>	240°C Maximum 2 Times / 230°C Maximum 1 Time
<b>Time within 5°C of actual peak (tp)</b>	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
<b>Ramp-down Rate</b>	5°C/Second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	N/A
<b>Moisture Sensitivity Level</b>	Level 1

### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum.

### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum.