



CMOS OCTAL BUS TRANSCIVER AND 3.3V TO 5V SHIFTER WITH 3-STATE OUT- PUTS AND 5 VOLT TOLERANT I/O

IDT74LVC4245A

FEATURES:

- 0.5 MICRON CMOS Technology
- $V_{CCA} = 5V \pm 0.5V$
- $V_{CCB} = 2.7V$ to $3.6V$
- CMOS power levels ($0.4\mu W$ typ. static)
- Rail-to-rail output swing for increased noise margin
- All inputs, outputs, and I/O are 5V tolerant
- Supports hot insertion
- Available in SOIC, SSOP, QSOP, and TSSOP packages

DRIVE FEATURES:

- High Output Drivers: $\pm 24mA$
- Reduced system switching noise

APPLICATIONS:

- 5V and 3.3V mixed voltage systems
- Data communication and telecommunication systems

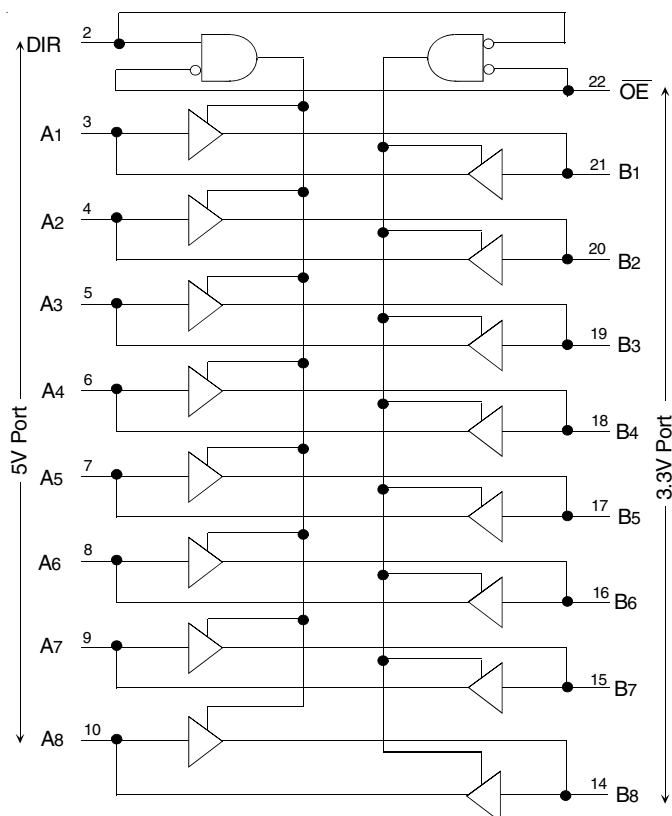
DESCRIPTION:

The LVC4245A is manufactured using advanced dual metal CMOS technology. This octal noninverting bus transceiver contains two separate supply rails; B port has V_{CCB} , which is set at 3.3V, and A port has V_{CCA} , which is set at 5V. This allows for translation from a 3.3V to a 5V environment, and vice-versa.

This device is ideal for asynchronous communication between two buses (A and B). The direction control pin (DIR) controls the direction of data flow. The output enable pin ($\overline{OE}$) overrides the direction control and disables both ports. All inputs are designed with hysteresis for improved noise margin.

The LVC4245A has been designed with a $\pm 24mA$ output driver. This driver is capable of driving a moderate to heavy load while maintaining speed performance.

FUNCTIONAL BLOCK DIAGRAM

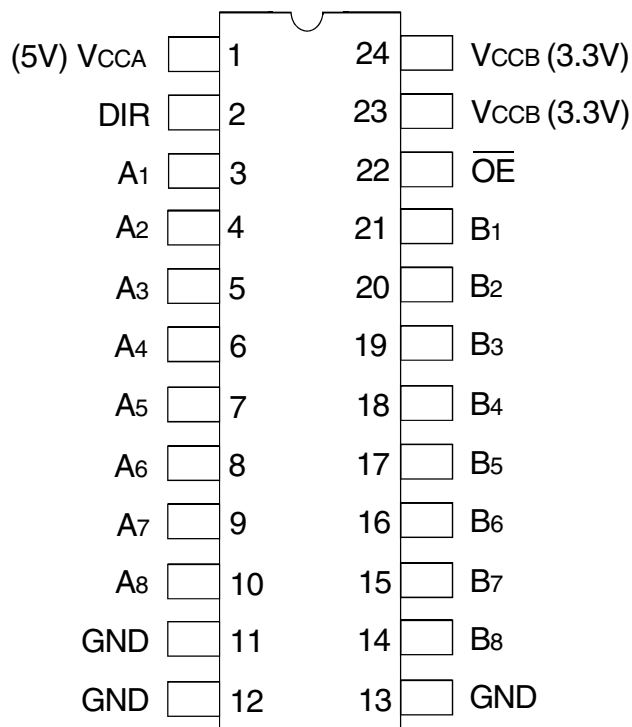


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INDUSTRIAL TEMPERATURE RANGE

February 20, 2009

PIN CONFIGURATION



SOIC/ SSOP/ QSOP/ TSSOP
TOP VIEW

ABSOLUTE MAXIMUM RATINGS FOR VCCB OR VCCB⁽¹⁾

Symbol	Description	Max	Unit
VTERM	Terminal Voltage with Respect to GND	−0.5 to +6.5	V
TSTG	Storage Temperature	−65 to +150	°C
IOUT	DC Output Current	−50 to +50	mA
IIK IOK	Continuous Clamp Current, Vi < 0 or Vo < 0	−50	mA
ICC ISS	Continuous Current through each Vcc or GND	±100	mA

NOTE:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

CAPACITANCE (TA = +25°C, F = 1.0MHz)

Symbol	Parameter ⁽¹⁾	Conditions	Typ.	Max.	Unit
CIN	Input Capacitance	VIN = 0V or VCCA VCCA = Open	5	—	pF
CI/O	I/O Port Capacitance ⁽²⁾	VOU = VCCA or GND VCCA = 5V	11	—	pF
CI/O	I/O Port Capacitance ⁽³⁾	VIN = VCCB or GND VCCB = 3.3V	11	—	pF

NOTES:

- As applicable to the device type.
- For A port only.
- For B port only.

PIN DESCRIPTION

Pin Names	Description
OE	Output Enable Input (Active LOW)
DIR	Direction Control Input
Ax	Port A Inputs or 3-State Outputs
Bx	Port B Inputs or 3-State Outputs

FUNCTION TABLE⁽¹⁾

Inputs		Outputs
OE	DIR	
L	L	Bus B Data to Bus A
L	H	Bus A Data to Bus B
H	X	High Z state

NOTE:

- H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't Care

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE (A PORT)

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CCA} = 5V \pm 0.5V^{(1)}$

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽²⁾	Max.	Unit
V_{IH}	Input HIGH Voltage Level	$V_{CCA} = 4.5V$ to $5.5V$		2	—	—	V
V_{IL}	Input LOW Voltage Level	$V_{CCA} = 4.5V$ to $5.5V$		—	—	0.8	V
I_{IH} I_{IL}	Input Leakage Current	$V_{CCA} = 5.5V$	$V_I = 0$ to $5.5V$	—	—	± 1	μA
I_{OZH} I_{OZL}	High Impedance Output Current (3-State Output pins)	$V_{CCA} = 5.5V$	$V_O = 0$ to $5.5V$	—	—	± 5	μA
V_H	Input Hysteresis	$V_{CCA} = 5V$		—	100	—	mV
I_{CCL} I_{CCH} I_{CCZ}	Quiescent Power Supply Current	$V_{CCA} = 5.5V$	$V_{IN} = GND$ or V_{CCA}	—	—	80	μA
ΔI_{CC}	Quiescent Power Supply Current Variation	One input at $3.4V$, other inputs at V_{CCA} or GND $V_{CCA} = 4.5V$ to $5.5V$		—	—	1.5	mA

NOTES:

- $V_{CCB} = 2.7V$ to $3.6V$.
- Typical values are at $V_{CCA} = 5V$, $+25^{\circ}\text{C}$ ambient.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE (B PORT)

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CCB} = 2.7V$ to $3.6V^{(1)}$

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽²⁾	Max.	Unit
V_{IH}	Input HIGH Voltage Level	$V_{CCB} = 2.7V$ to $3.6V$		2	—	—	V
V_{IL}	Input LOW Voltage Level	$V_{CCB} = 2.7V$ to $3.6V$		—	—	0.8	V
I_{OZH} I_{OZL}	High Impedance Output Current (3-State Output pins)	$V_{CCB} = 3.6V$	$V_O = 0$ to V_{CCB}	—	—	± 5	μA
V_H	Input Hysteresis	$V_{CCB} = 3.3V$		—	100	—	mV
I_{CCL} I_{CCH} I_{CCZ}	Quiescent Power Supply Current	$V_{CCB} = 3.6V$	$V_{IN} = GND$ or V_{CCB}	—	—	50	μA
ΔI_{CC}	Quiescent Power Supply Current Variation	One input at $V_{CCB} - 0.6V$, other inputs at V_{CCB} or GND $V_{CCB} = 2.7V$ to $3.6V$		—	—	500	μA

NOTES:

- $V_{CCA} = 5V \pm 0.5V$.
- Typical values are at $V_{CCB} = 3.3V$, $+25^{\circ}\text{C}$ ambient.

OUTPUT DRIVE CHARACTERISTICS, $V_{CCA} = 5V \pm 0.5V$ (A PORT)

Symbol	Parameter	Test Conditions ⁽¹⁾		Min.	Max.	Unit
VOH	Output HIGH Voltage (B Port to A Port)	VCCA = 4.5V	IOH = - 0.1mA	4.3	—	V
		VCCA = 5.5V		5.3	—	
		VCCA = 4.5V	IOH = - 24mA	3.7	—	
		VCCA = 5.5V		4.7	—	
VOL	Output LOW Voltage (B Port to A Port)	VCCA = 4.5V	IOL = 0.1mA	—	0.2	V
		VCCA = 5.5V		—	0.2	
		VCCA = 4.5V	IOL = 24mA	—	0.55	
		VCCA = 5.5V		—	0.55	

NOTE:
1. VIH and VIL must be within the min. or max. range shown in the DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE table for the appropriate Vcc range.
TA = - 40°C to + 85°C, VCCB = 2.7V to 3.6V.

OUTPUT DRIVE CHARACTERISTICS, $V_{CCB} = 2.7V$ TO $3.6V$ (B PORT)

Symbol	Parameter	Test Conditions ⁽¹⁾		Min.	Max.	Unit
VOH	Output HIGH Voltage (A Port to B Port)	VCCB = 2.7V to 3.6V	IOH = - 0.1mA	VCC - 0.2	—	V
		VCCB = 2.7V	IOH = - 12mA	2.2	—	
		VCCB = 3V		2.4	—	
		VCCB = 3V	IOH = - 24mA	2	—	
VOL	Output LOW Voltage (A Port to B Port)	VCCB = 2.7V to 3.6V	IOL = 0.1mA	—	0.2	V
		VCCB = 2.7V	IOL = 12mA	—	0.4	
		VCCB = 3V	IOL = 24mA	—	0.55	

NOTE:
1. VIH and VIL must be within the min. or max. range shown in the DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE table for the appropriate Vcc range.
TA = - 40°C to + 85°C, VCCA = 5V $\pm$ 0.5V.

OPERATING CHARACTERISTICS, TA = 25°C

Symbol	Parameter	Test Conditions	VCCA = 5V, VCCB = 3.3V	Unit
			Typical	
CPD	Power Dissipation Capacitance per Transceiver Outputs enabled	CL = 0pF, f = 10Mhz	39.5	pF
CPD	Power Dissipation Capacitance per Transceiver Outputs disabled		5	

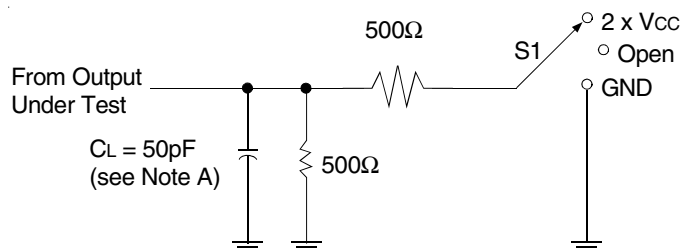
SWITCHING CHARACTERISTICS⁽¹⁾

Symbol	Parameter	VCCA = 5V ± 0.5V VCCB = 2.7V to 3.6V		Unit
		Min.	Max.	
tPLH	Propagation Delay	1	6.3	ns
tPHL	Ax to Bx	1	6.7	
tPLH	Propagation Delay	1	6.1	ns
tPHL	Bx to Ax	1	5	
tPZL	Output Enable Time	1	8.8	ns
tPLZ	$\overline{OE}$ to Bx	1	9.8	
tPZL	Output Enable Time	1	9	ns
tPLZ	$\overline{OE}$ to Ax	1	8.1	
tPLZ	Output Disable Time	1	7.7	ns
tPHZ	$\overline{OE}$ to Bx	1	7.8	
tPLZ	Output Disable Time	1	7	ns
tPHZ	$\overline{OE}$ to Ax	1	5.8	

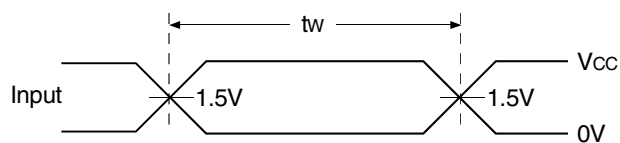
NOTE:

1. See TEST CIRCUITS AND WAVEFORMS. $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$.

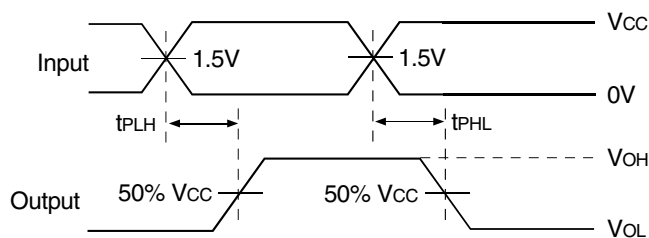
LOAD CIRCUIT AND VOLTAGE WAVEFORMS PARAMETER MEASUREMENT INFORMATION (A PORT)



Load Circuit



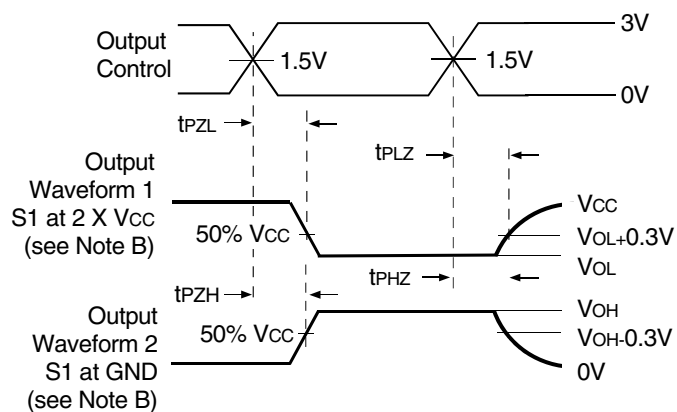
Voltage Waveforms Pulse Duration



Voltage Waveforms Propagation Delay Times
Noninverting Outputs

TEST CONDITIONS

TEST	S1
t_{PLH} / t_{PHL}	Open
t_{PLZ} / t_{PZL}	2 x V_{CC}
t_{PHZ} / t_{PZH}	GND

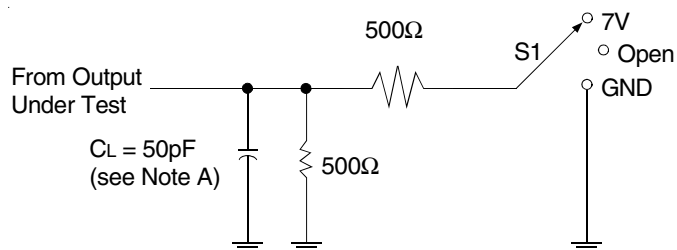


Voltage Waveforms Enable and Disable Times
Low- and High-Level Enabling

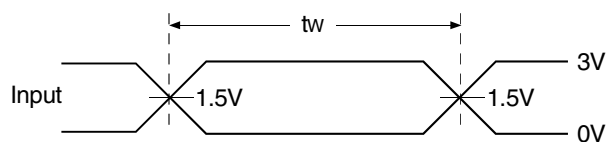
NOTES:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is LOW except when disabled by the output control.
Waveform 2 is for an output with internal conditions such that the output is HIGH except when disabled by the output control.
- All input pulses are supplied by generators having the following characteristics: $PRR \leq 10\text{MHz}$; $Z_o = 50\Omega$; $t_r \leq 2.5\text{ns}$; $t_f \leq 2.5\text{ns}$.
- The outputs are measured one at a time with one transition per measurement.

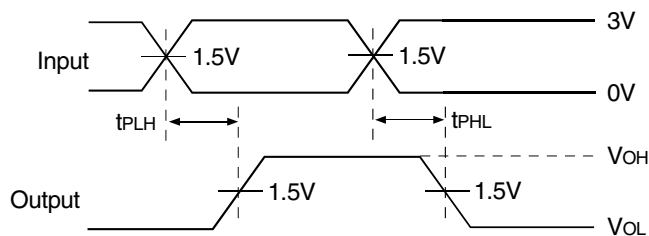
LOAD CIRCUIT AND VOLTAGE WAVEFORMS PARAMETER MEASUREMENT INFORMATION (B PORT)



Load Circuit



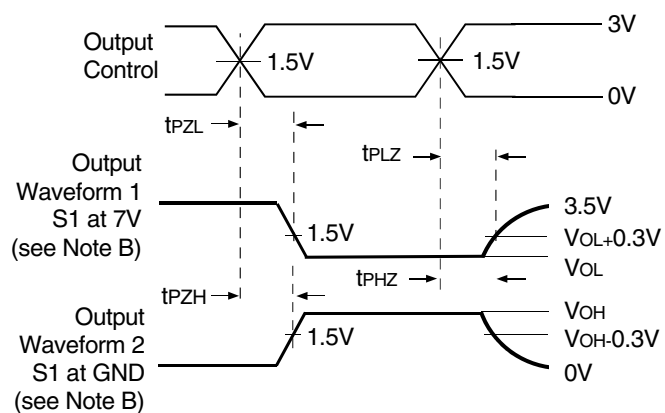
Voltage Waveforms Pulse Duration



Voltage Waveforms Propagation Delay Times
Noninverting Outputs

TEST CONDITIONS

TEST	S1
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	7V
t_{PHZ}/t_{PZH}	GND



Voltage Waveforms Enable and Disable Times
Low- and High-Level Enabling

NOTES:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is LOW except when disabled by the output control.
Waveform 2 is for an output with internal conditions such that the output is HIGH except when disabled by the output control.
- All input pulses are supplied by generators having the following characteristics: $PRR \leq 10\text{MHz}$; $Z_o = 50\Omega$; $t_r \leq 2.5\text{ns}$; $t_f \leq 2.5\text{ns}$.
- The outputs are measured one at a time with one transition per measurement.

ORDERING INFORMATION

XX	LVC	X	XXXX	XX	
Temp. Range	Bus-Hold	Device Type	Package		
				SO	Small Outline IC (gull wing)
				SOG	SOIC - Green
				PY	Shrink Small Outline Package
				PYG	SSOP - Green
				Q	Quarter Size Small Outline Package
				QG	QSOP - Green
				PG	Thin Shrink Small Outline Package
				PGG	TSSOP - Green
			4245A		Octal Bus Transceiver and 3.3V to 5V Shifter with 3-State Outputs, $\pm 24\text{mA}$
			Blank		No Bus-hold
			74		-40°C to $+85^{\circ}\text{C}$



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