

NOTES:

1. MATERIALS

HOUSING - HIGH TEMP. NYLON GLASS FILLED, UL 94 V-0, BLACK.
EJECTOR - HIGH TEMP. NYLON GLASS FILLED, UL 94 V-0, BLACK.
TERMINAL - COPPER ALLOY

2. PLATING - SEE TABLE IN SHEET 5.

3. CARD SLOT ACCEPTS 1.27 ± 0.10 MM MODULE THICKNESS.
(MEASURED OVER P.C. PADS)

4. RECOMMENDED MODULE LAYOUT SHALL BE AS PER JEDEC MO-237.

5. REFER TO PRODUCT SPEC. PS-87978-013 FOR PERFORMANCE SPECIFICATIONS.

6. PRODUCT SHALL BE PACKED IN TRAY.

7. PRODUCT SHALL HAVE DATE CODE STAMPED ON SIDE OF HOUSING.

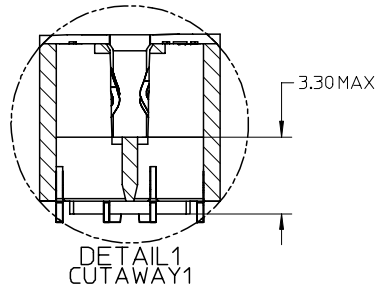
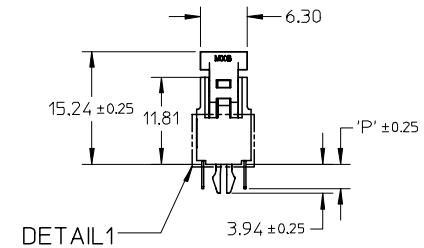
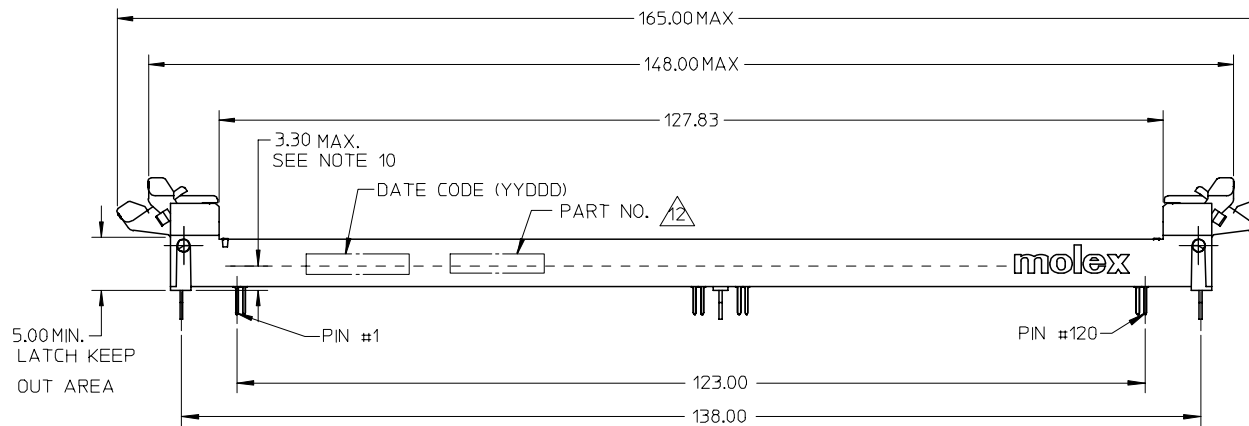
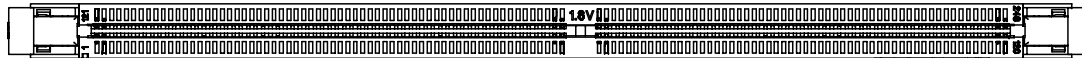
8. KEEP OUT AREA IS THE AREA WHERE THE CONNECTOR IS MOUNTED ONTO THE PCB WHICH DOES NOT INCLUDE THE LATCH.

9. REFER TO CRITICAL JEDEC INFORMATION FOR SOCKET OUTLINE PER SO-001.

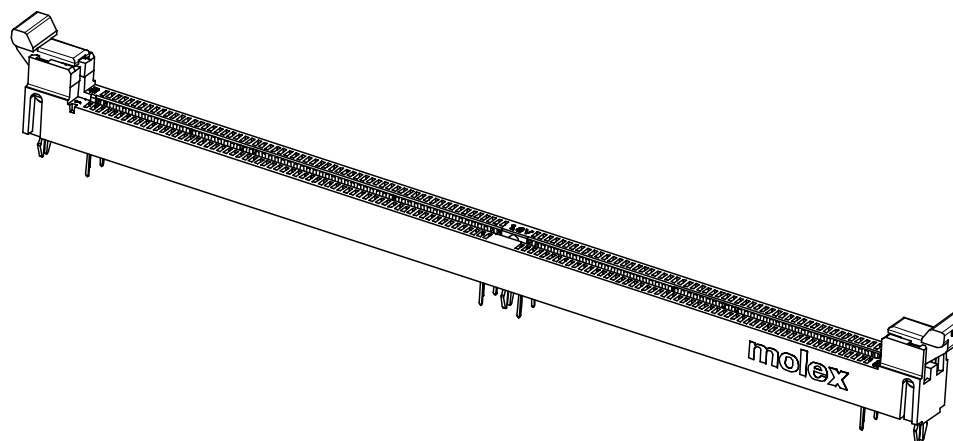
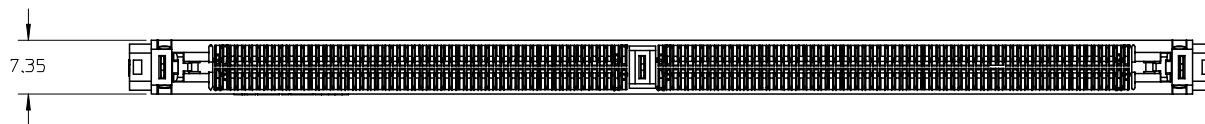
10. DIMM MODULE SEATING PLANE FROM TOP OF PCB.

11. CONTACT MOLEX FOR AVAILABILITY OF PRODUCT OPTIONS.

12. PART NUMBER SHALL BE MARKED LEGIBLY AS 87978-****



REVISED		QUALITY SYMBOLS		GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
EC NO:	DATE	DRWN:	CHKD:	APPR:	REV	mm	INCH	DRAWN BY	DATE	TITLE	
S2008-0235	2007/09/27	CCTEH	CCTEH	SHLENI		4 PLACES ± ---	± ---	CCTEH	2005/01/03	DDR2 DIMM, 1MM PITCH 240 CKTS, FORK-LOCK VERS VLP LATCH MOLEX INCORPORATED	
	2007/09/27					3 PLACES ± ---	± ---	CHECKED BY	DATE		
						2 PLACES ± 0.20	± ---	SRRAMESH	2005/08/30		
						1 PLACE ± ---	± ---	APPROVED BY	DATE		
						ANGULAR ± 5 °		GGLEE	2005/08/30	MOLEX INCORPORATED	
						DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		MATERIAL NO.		DOCUMENT NO.	SHEET NO.
								SEE TABLE		SD-87978-001	1 OF 5
								THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			



REVISED EC NO: S2008-0235 DRWN:CMTEO 2007/09/27 CHKD:CCTEH 2007/09/27 APPR:SHLENI 2007/09/28	DESCRIPTION REV	QUALITY SYMBOLS ▽=0 ▽C=0	GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY CCTEH	DATE 2005/01/03	TITLE DDR2 DIMM, 1MM PITCH 240 CKTS, FORK-LOCK VERS VLP LATCH				
			4 PLACES	± ---	± ---	CHECKED BY SRRAMESH	DATE 2005/08/30					
			3 PLACES	± ---	± ---	APPROVED BY GGLEE	DATE 2005/08/30	MOLEX INCORPORATED				
			2 PLACES	± 0.20	± ---	MATERIAL NO.						
			1 PLACE	± ---	± ---	SEE TABLE		SD-87978-001		2 OF 5		
			ANGULAR ± 5 °			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS									

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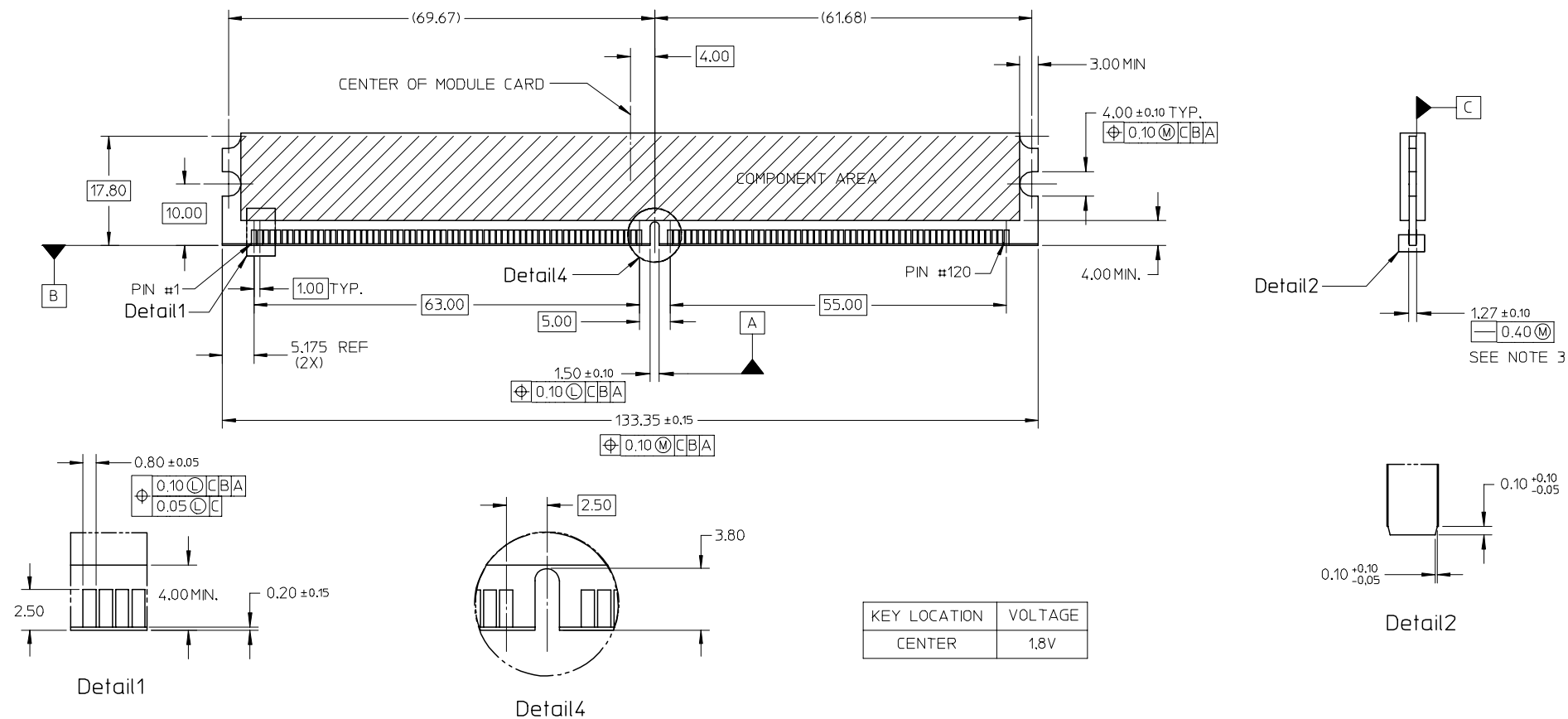
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RECOMMENDED MODULE LAYOUT



NOTES:
1. MODULE CARD USED FOR TESTS ARE WITH 0.10MM CHAMFER.

REVISED EC NO: S2008-0235 DRWN:CMTEO 2007/09/27 CHKD:CTEH 2007/09/27 APPR:SHENI 2007/09/28	DESCRIPTION	QUALITY SYMBOLS ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY	DATE	TITLE DDR2 DIMM, 1MM PITCH 240 CKTS, FORK-LOCK VERS VLP LATCH	molex MOLEX INCORPORATED	DOCUMENT NO. SD-87978-001	SHEET NO. 3 OF 5
			4 PLACES	± ---	± ---	CTEH	2005/01/03				
			3 PLACES	± ---	± ---	CHECKED BY	DATE				
			2 PLACES	± 0.20	± ---	SRRAMESH	2005/08/30				
			1 PLACE	± ---	± ---	APPROVED BY	DATE				
			ANGULAR ± 5 °		GGLEE	2005/08/30					
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		MATERIAL NO.						
					SEE TABLE						
					SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

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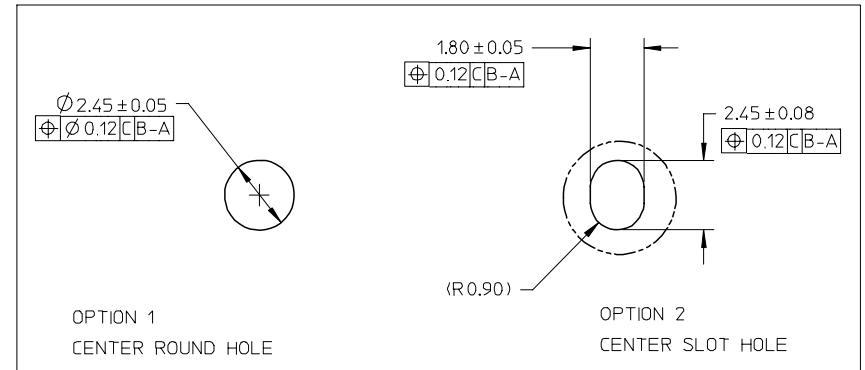
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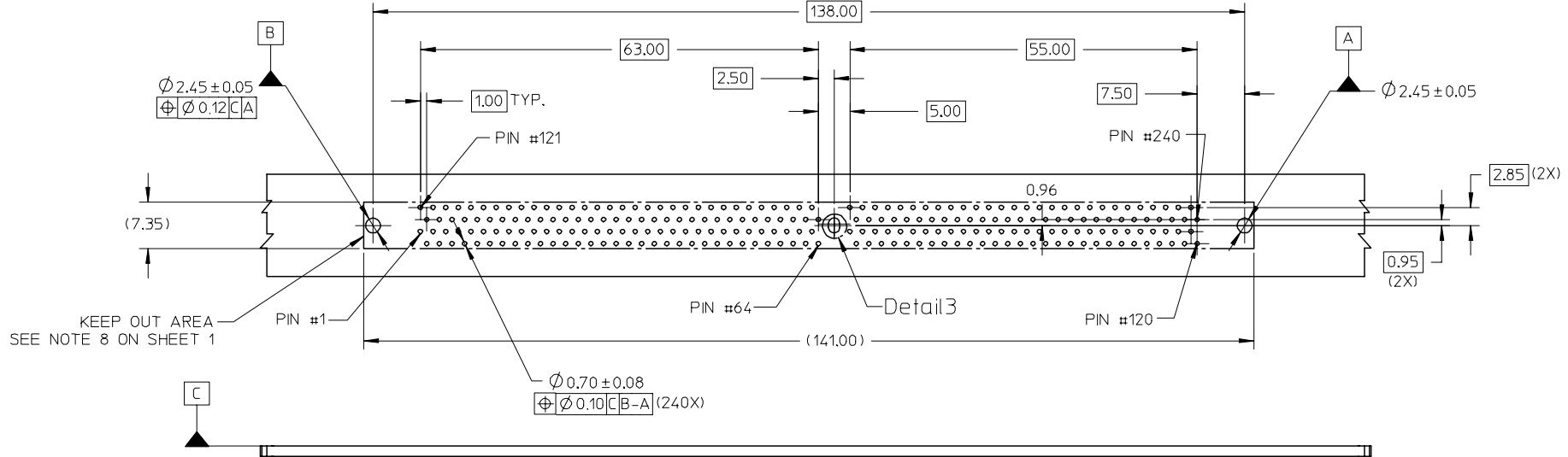
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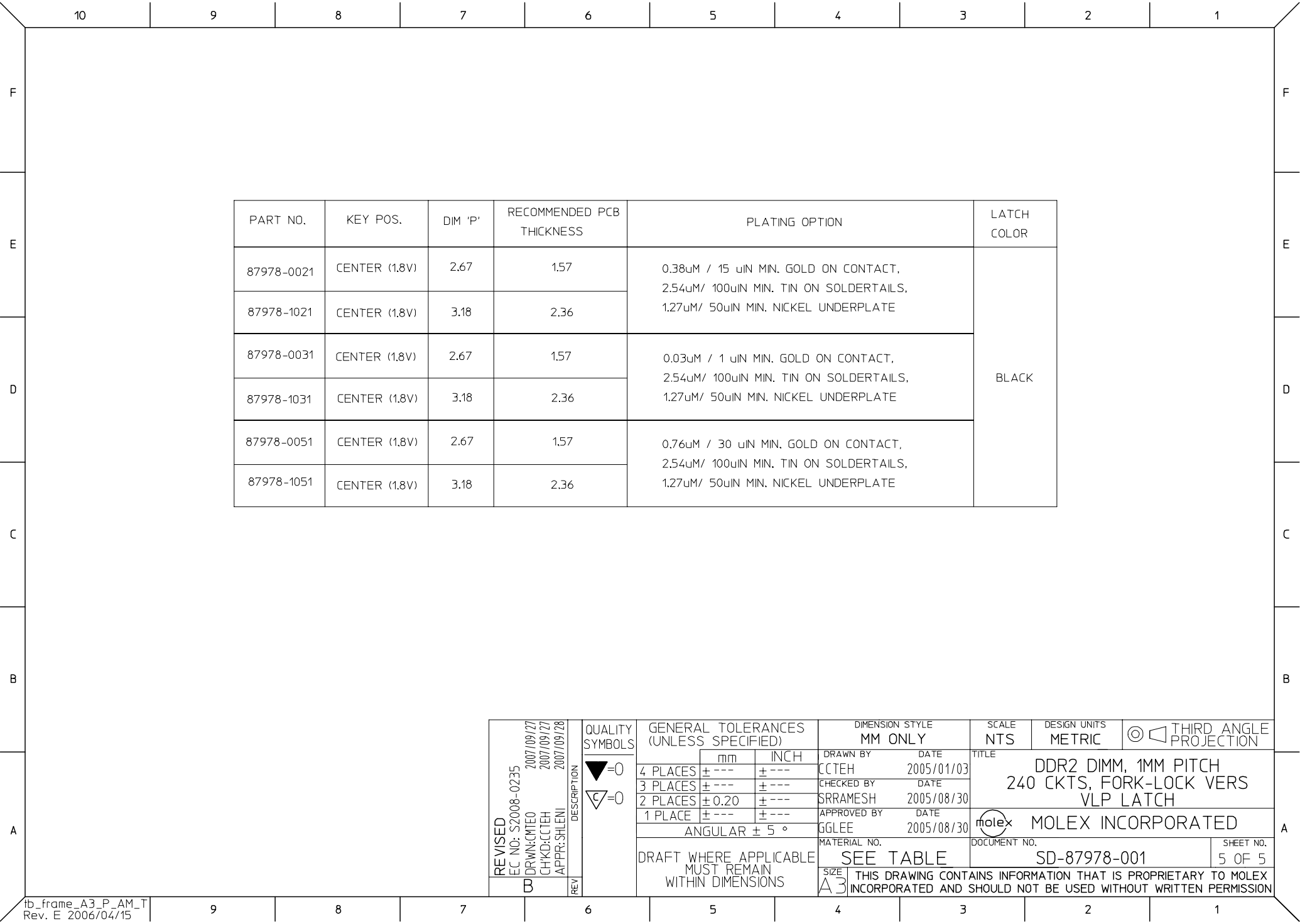


(SEE RECOMMENDED THICKNESS IN TABLE)
RECOMMENDED PCB LAYOUT
CONNECTOR SIDE

Detail3



REVISED EC NO: S2008-0235 2007/09/27 DRWN:CMTEO CHKD:CTEH 2007/09/27 APPR:SHLENI 2007/09/28	DESCRIPTION REV	QUALITY SYMBOLS ▽=0 ▽C=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
					DRAWN BY CTEH	DATE 2005/01/03	TITLE DDR2 DIMM, 1MM PITCH 240 CKTS, FORK-LOCK VERS VLP LATCH					
					CHECKED BY SRRAMESH	DATE 2005/08/30						
					APPROVED BY GGLEE	DATE 2005/08/30						MOLEX INCORPORATED
					ANGULAR ± 5 °		MATERIAL NO.		DOCUMENT NO.		SHEET NO.	
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE		SD-87978-001		4 OF 5			
			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION									



PART NO.	KEY POS.	DIM 'P'	RECOMMENDED PCB THICKNESS	PLATING OPTION	LATCH COLOR
87978-0021	CENTER (1.8V)	2.67	1.57	0.38uM / 15 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE	BLACK
87978-1021	CENTER (1.8V)	3.18	2.36		
87978-0031	CENTER (1.8V)	2.67	1.57	0.03uM / 1 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE	
87978-1031	CENTER (1.8V)	3.18	2.36		
87978-0051	CENTER (1.8V)	2.67	1.57	0.76uM / 30 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE	
87978-1051	CENTER (1.8V)	3.18	2.36		

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				mm	INCH	DRAWN BY	DATE	TITLE DDR2 DIMM, 1MM PITCH 240 CKTS, FORK-LOCK VERS VLP LATCH				
			4 PLACES	± ---	± ---	CCTEH	2005/01/03					
			3 PLACES	± ---	± ---	CHECKED BY	DATE					
			2 PLACES	± 0.20	± ---	SRRAMESH	2005/08/30	MOLEX INCORPORATED				
			1 PLACE	± ---	± ---	APPROVED BY	DATE					
					ANGULAR ± 5 °		GGLEE	2005/08/30	SD-87978-001			
				MATERIAL NO.		DOCUMENT NO.		SHEET NO.				
B	REV	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE								
				SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						