

Power-saving Type

Absolute maximum rating
Supply Voltage (V _{CC}) -0.5 to +7.0 V
Storage Temperature Range -55 to +125 °C



Item			Model	2725Q							
Output Level				CMOS							
Nominal Frequency Range			(MHz)	2.5 ≤ F < 5	5 ≤ F < 10	10 ≤ F < 20	20 ≤ F < 40	40 ≤ F < 75	75 ≤ F ≤ 125		
Operating Temperature Range			(°C)	-10 to +70							
Overall Frequency Tolerance			(×10 ⁻⁶)	±50							
Supply Voltage [V _{CC}]			(V)	+2.5±0.1							
Current Consumption Max.	During Operation	+25 °C	(mA)	2	3	4	6	16	24		
	During Standby	+25 °C	(μA)	10							
V _{OL} max/V _{OH} min			(V)	0.1 V _{CC} /0.9 V _{CC}						0.2 V _{CC} /0.8 V _{CC}	
Tr Max. / Tf Max.			(ns)	5/5 (0.1 V _{CC} to 0.9 V _{CC})						4/4 (0.2 V _{CC} to 0.8 V _{CC})	
Symmetry Min. to Max.			(%)	40 to 60 (at 1/2 V _{CC})							
Load (C _L) Max.			(pF)	15							
Start-up time Max.			(ms)	4				10			
Standby function				Available (Three-state)							
Specification Number				NSA6296B							

mm

Terminal land connections

#1	STAND-BY
#2	GND
#3	OUTPUT
#4	V _{CC}

Top view dimensions: 5.0±0.2, 3.2±0.2

Side view dimensions: 1.0±0.2, 0.36±0.1

Land pattern (Typical) dimensions: 1.2±0.1, 1.2±0.1, 0.25±0.1, 2.2, 1.4, 1.2, 2.54, 2.54±0.1, 1.0±0.1, C0.5, #1, #2, #3, #4

Cby=0.01μF

Mount an approx. 0.01 μF bypass capacitor between V_{CC} and GND (close to the product)

The diagram shows a square wave signal. The vertical axis represents voltage with levels: $0.9V_{CC}$ or $0.8V_{CC}$, $1/2V_{CC}$, $0.1V_{CC}$ or $0.2V_{CC}$, V_{OL} , and $0V$. The horizontal axis represents time. Key parameters are labeled: T_r (rise time), T_f (fall time), T (period), and t (duty cycle). The signal transitions from V_{OL} to V_{OH} and back. The duty cycle t is the duration of the high state, and the period T is the total duration of one cycle.

#1 Input	#3 Output
Level H ($0.7 V_{CC} \leq V_{IH} \leq V_{CC}$) or OPEN is selected.	Oscillation output ON
Level L ($V_{IL} \leq 0.3 V_{CC}$) is selected.	High impedance

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