

DISTINCTIVE CHARACTERISTICS**Advanced LED Illumination with:**

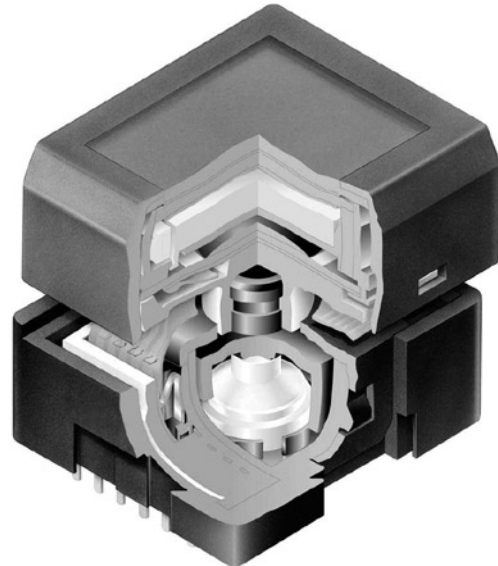
- Broad and even light distribution
- Consistent bright backlighting
- Low energy consumption

Programmable LC Display

Yellow/Green LED Backlighting

Rubber Dome

Epoxy Sealed Straight PC Terminals



Compact model saves space and accommodates smaller form factor design requirements.
Programmable to display graphics, alphanumeric characters, and animated sequences.
Integrated liquid crystal display provides wide viewing angle with high contrast and clarity.
Viewing area 13.0mm x 10.7mm (horizontal x vertical) at 36 x 24 pixels.
Dome gives crisp tactile feedback to positively indicate circuit transfer.
High reliability and long life of one million actuations minimum.
Epoxy sealed terminals prevent entry of solder flux and other contaminants.

SWITCH SPECIFICATIONS

Circuit	SPST normally open
Electrical Capacity (Resistive Load)	100mA @ 12V DC
Contact Resistance	200 milliohms maximum @ 20mV 10mA
Insulation Resistance	100 megohms minimum @ 100V DC
Dielectric Strength	125V AC for 1 minute minimum
Mechanical Endurance	1,000,000 operations minimum
Electrical Endurance	1,000,000 operations minimum
Operating Force	2.2 ± 0.5 Newtons
Total Travel	1.8mm (.071")
Operating Temperature Range	0°C ~ +40°C (+32°F ~ +104°F)
Storage Temperature Range	-10°C ~ +60°C (+14°F ~ +140°F)

LCD SPECIFICATIONS

Characteristics of Display

Display Operation Mode	STN positive
Display Condition	Transflective with built-in LED backlight
Viewing Angle	Adjustable
Driving Method	1/24 duty, 1/5 bias (built-in driving circuit)
Viewing Area	13.0mm x 10.7mm (horizontal x vertical)
Pixel Format	36 x 24 dots (horizontal x vertical)
Pixel Size	0.32mm x 0.32mm (horizontal x vertical)
Backlight LED	Yellow/Green



Yellow/Green Bicolor LED
with Yellow LCD Mode

Absolute Maximum Ratings (Temperature at 25°C)

Items	Symbols	Ratings
Supply Voltage for Logics	V_{DD}	-0.3V to +7.0V
Supply Voltage for LCD	V_{LC}	-0.3V to +12.0V
Input Voltage	V_I	-0.3V to V_{DD} +0.3V
Output Voltage	V_O	-0.3V to V_{DD} +0.3V

Recommended Operating Conditions (Temperature at 25°C)

Items	Symbols	Minimum	Typical	Maximum
Supply Voltage for Logics	V_{DD}	4.5V	5.0V	5.5V
Supply Voltage Yellow	V_{LC}	—	7.3V	—
Input Voltage	V_I	0V	—	V_{DD}
Driving Frequency	f_{FLM}	—	150Hz	—

DC Characteristics of LCD Drive IC (Temperature at 0°C to 40°C and $V_{DD} = 5.0V \pm 10\%$)

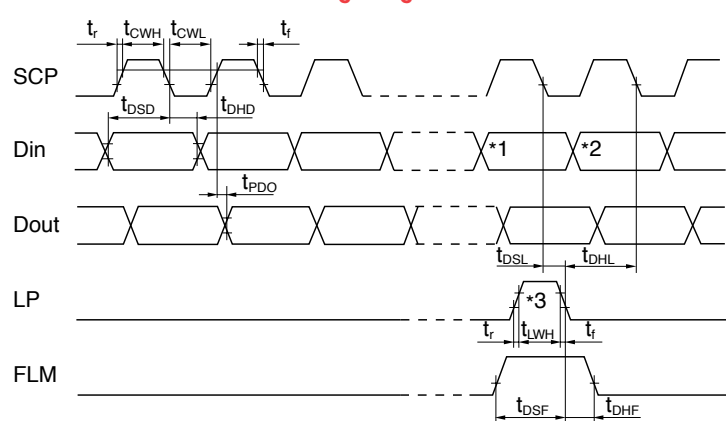
Items	Symbols	Test Conditions	Minimum	Typical	Maximum	Unit
High Level Input Voltage	V_{IH}		0.7 V_{DD}		V_{DD}	V
Low Level Input Voltage	V_{IL}		0		0.3 V_{DD}	V
High Level Input Leakage Current	I_{LIH}	$V_I = V_{DD}$			10	μA
Low Level Input Leakage Current	I_{LIL}	$V_I = 0V$			-10	μA
High Level Output Voltage	V_{OH}	$I_{OH} = -500\mu A$	$V_{DD} - 0.5$			V
Low Level Output Voltage	V_{OL}	$I_{OL} = 500\mu A$			0.5	V
High Level Output Leakage Current	I_{LOH}	$V_O = V_{DD}$			10	μA
Low Level Output Leakage Current	I_{LOL}	$V_O = 0V$			-10	μA
Supply Current	I_{DD}	$f_{SCP} = 1.0MHz$			500	μA
LCD Drive Current	I_{LC}	$f_{LP} = 2.4kHz$ $V_{LC} = 7.3V \sim 7.5V$		500	2,000	μA

Timing Characteristics of LCD Drive IC

(Temperature at 0°C to 40°C and $V_{DD} = 5.0V \pm 10\%$)

Items	Symbols	Minimum	Maximum
Clock Operation Frequency	f_{SCP}		6.0MHz
Latch Pulse Frequency	f_{LP}		50kHz
Clock High Level Pulse Width	t_{CWH}	70ns	
Clock Low Level Pulse Width	t_{CWL}	70ns	
Data Setup Time	t_{DSD}	45ns	
Data Hold Time	t_{DHD}	50ns	
Data Output Delay Time	t_{PDO}		25ns
Latch Setup Time	t_{DSL}	50ns	
Latch Hold Time	t_{DHL}	50ns	
Latch High Level Width	t_{LWH}	200ns	
FLM Setup Time	t_{DSF}	50ns	
FLM Hold Time	t_{DHF}	50ns	
SCP, LP Rise/Fall Time	t_r/t_f		15ns

Timing Diagram



- *1 Last data on first line
- *2 Beginning data on second line
- *3 Location of LP signal on first line

The block diagram illustrates the internal architecture of the LCD module. It features a 36 x 24 Dot Matrix LCD Panel connected to a Common Driver and a Segment Driver. The Common Driver is controlled by a 24-bit Shift Register (D, CK) and a Timing Control block. The Segment Driver is controlled by a 40-bit Latch (CK) and a 40-bit Shift Register (CK, D). The Timing Control block is also connected to the 40-bit Shift Register. The 40-bit Shift Register has a D input and a Q output (Dout). The Segment Driver outputs are labeled seg1 through seg36. The LCD Panel outputs are labeled Com1 through Com24. The module is powered by V_{LC} (pin 12) and V_{DD} (pin 11). It has control pins FLM (pin 6), SCP (pin 8), and LP (pin 7). Data pins include Din (pin 9) and Dout (pin 5). Ground (GND) is connected to pin 10. The module also includes a switch (SW) connected to pins 1 and 2, and BL-LED (+) (pin 13) connected to pins 3 and 14, and BL-LED (-) (pin 14) connected to pin 13. Pin 4 is labeled NC (Not Connected).

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SUPER BRIGHT LED SPECIFICATIONS

Typical Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Yellow/Green	Unit
Forward Current	I_F	15/15	mA
Forward Voltage	V_F	2.2/3.3	V

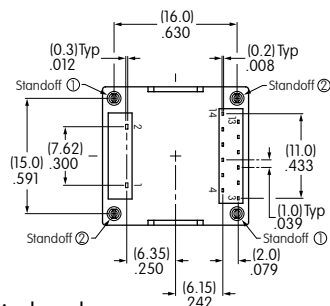
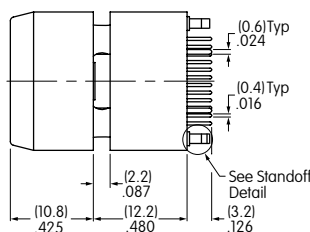
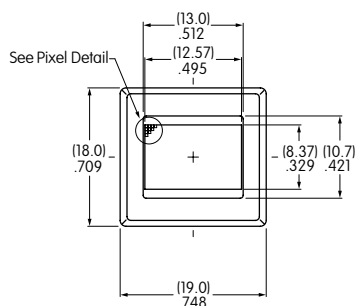
ABSOLUTE MAXIMUM FOR LEDS

Electrical Characteristics (Temperature at 25°C)

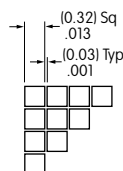
Backlight Color	Symbols	Yellow/Green	Unit
Forward Current	I_F	20	mA
Reverse Voltage	V_R	4.0	V
Current Reduction Rate Above 25°C	$\Delta I_F(\text{DC})$	-0.26	mA/°C
Power Dissipation*	P_D	130 maximum	mW

*For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating.

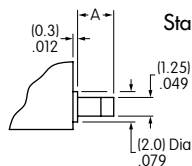
TYPICAL SWITCH DIMENSIONS



Terminal numbers are not on the switch.



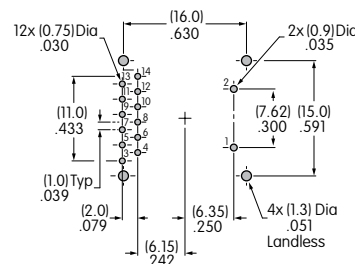
Pixel Detail



Standoff Detail

Dimension A

Standoff 1 = (2.7) .106 Standoff 2 = (2.3) .091



Footprint

PRECAUTIONS FOR HANDLING & STORAGE

Handling

1. The VLC voltage should not be applied before logic voltage. If VLC voltage is present before logic voltage, it may cause the driver logic to freeze and damage the LCD, and the driver logic itself may become damaged.
2. The IS Series devices are electrostatic sensitive.
3. Limit operating force to keytop to 100.0N maximum, as excessive pressure may damage LCD device.
4. Recommended soldering time and temperature limits are 5 seconds maximum @ 270°C maximum.
5. Do not exceed 60°C at the LCD level.
6. The IS series devices are not process sealed.
7. If the LCD is accidentally broken, avoid contact with the liquid and wash off any liquid spills to the skin or clothing.
8. Clean cap surface with dry cloth. If further cleaning is needed, wipe with dampened cloth using neutral cleanser and dry with clean cloth. Do not use organic solvent.



Storage

1. Store away from direct sunlight.
2. Keep away from static electricity.
3. Avoid extreme temperatures, high humidity, gaseous substances, and all forms of chemical contamination.