

DESCRIPTION

The H11DX and 4N38 are phototransistor-type optically coupled optoisolators. An infrared emitting diode manufactured from specially grown gallium arsenide is selectively coupled with a high voltage NPN silicon phototransistor. The device is supplied in a standard plastic six-pin dual-in-line package.

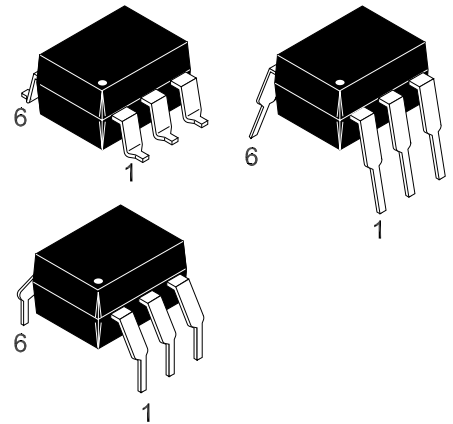
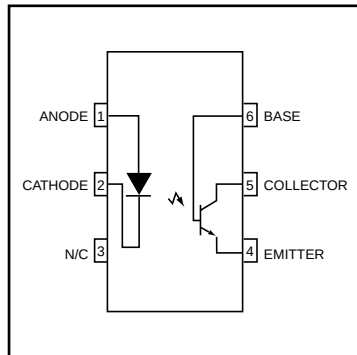
**H11D1
H11D2
H11D3
H11D4
4N38**

FEATURES

- High Voltage
 - H11D1, H11D2, $BV_{CER} = 300\text{ V}$
 - H11D3, H11D4, $BV_{CER} = 200\text{ V}$
- High isolation voltage
 - 5300 VAC RMS - 1 minute
 - 7500 VAC PEAK - 1 minute
- Underwriters Laboratory (UL) recognized File# E90700

APPLICATIONS

- Power supply regulators
- Digital logic inputs
- Microprocessor inputs
- Appliance sensor systems
- Industrial controls



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Units
TOTAL DEVICE			
Storage Temperature	T_{STG}	-55 to +150	°C
Operating Temperature	T_{OPR}	-55 to +100	°C
Lead Solder Temperature	T_{SOL}	260 for 10 sec	°C
Total Device Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	260	mW
Derate above 25°C		3.5	mW/°C
EMITTER			
*Forward DC Current	I_F	80	mA
*Reverse Input Voltage	V_R	6.0	V
*Forward Current - Peak (1μs pulse, 300pps)	$I_F(pk)$	3.0	A
*LED Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	150	mW
Derate above 25°C		1.41	mW/°C

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ABSOLUTE MAXIMUM RATINGS (Cont.)

Parameter	Symbol	Value	Units
DETECTOR			
*Power Dissipation @ $T_A = 25^\circ\text{C}$	P_D	300	mW
Derate linearly above 25°C		4.0	mW/ $^\circ\text{C}$
*Collector to Emitter Voltage	V_{CER}	H11D1 - H11D2 H11D3 - H11D4 4N38	300 200 80
*Collector Base Voltage		H11D1 - H11D2 H11D3 - H11D4 4N38	300 200 80
*Emitter to Collector Voltage		H11D1 - H11D2 H11D3 - H11D4	7
Collector Current (Continuous)		100	mA

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ Unless otherwise specified.)

INDIVIDUAL COMPONENT CHARACTERISTICS

Characteristic	Test Conditions	Symbol	Device	Min	Typ**	Max	Unit
EMITTER							
*Forward Voltage	($I_F = 10\text{ mA}$)	V_F	ALL		1.15	1.5	V
Forward Voltage Temp. Coefficient		$\frac{\Delta V_F}{\Delta T_A}$	ALL		-1.8		mV/ $^\circ\text{C}$
Reverse Breakdown Voltage	($I_R = 10\text{ }\mu\text{A}$)	BV_R	ALL	6	25		V
Junction Capacitance	($V_F = 0\text{ V}$, $f = 1\text{ MHz}$)	C_J	ALL		50		pF
	($V_F = 1\text{ V}$, $f = 1\text{ MHz}$)		ALL		65		pF
*Reverse Leakage Current	($V_R = 6\text{ V}$)	I_R	ALL		0.05	10	μA
DETECTOR							
*Breakdown Voltage Collector to Emitter	($R_{BE} = 1\text{ M}\Omega$)	BV_{CER}	H11D1/2	300			V
	($I_C = 1.0\text{ mA}$, $I_F = 0$)		H11D3/4	200			
	(No R_{BE}) ($I_C = 1.0\text{ mA}$)	BV_{CEO}	4N38	80			
*Collector to Base	($I_C = 100\text{ }\mu\text{A}$, $I_F = 0$)	BV_{CBO}	H11D1/2	300			
			H11D3/4	200			
			4N38	80			
Emitter to Base	($I_E = 100\text{ }\mu\text{A}$, $I_F = 0$)	BV_{EBO}	4N38	7			
Emitter to Collector		BV_{ECO}	ALL	7	10		
*Leakage Current Collector to Emitter ($R_{BE} = 1\text{ M}\Omega$)	($V_{CE} = 200\text{ V}$, $I_F = 0$, $T_A = 25^\circ\text{C}$)	I_{CER}	H11D1/2			100	nA
	($V_{CE} = 200\text{ V}$, $I_F = 0$, $T_A = 100^\circ\text{C}$)					250	μA
	($V_{CE} = 100\text{ V}$, $I_F = 0$, $T_A = 25^\circ\text{C}$)		H11D3/4			100	nA
	($V_{CE} = 100\text{ V}$, $I_F = 0$, $T_A = 100^\circ\text{C}$)					250	μA
	(No R_{BE}) ($V_{CE} = 60\text{ V}$, $I_F = 0$, $T_A = 25^\circ\text{C}$)	I_{CEO}	4N38			50	nA

Notes

* Parameters meet or exceed JEDEC registered data (for 4N38 only)

** All typical values at $T_A = 25^\circ\text{C}$

H11D1, H11D2, H11D3, H11D4, 4N38

TRANSFER CHARACTERISTICS

DC Characteristic	Test Conditions	Symbol	Device	Min	Typ**	Max	Unit	
EMITTER Current Transfer Ratio Collector to Emitter	(I _F = 10 mA, V _{CE} = 10 V) (R _{BE} = 1 MΩ)	CTR	H11D1 H11D2 H11D3	2 (20)			mA (%)	
			H11D4	1 (10)				
	(I _F = 10 mA, V _{CE} = 10 V)		4N38	2 (20)				
	*Saturation Voltage	(I _F = 10 mA, I _C = 0.5 mA) (R _{BE} = 1 MΩ)	V _{CE (SAT)}	H11D1/2/3/4		0.1	0.40	V
(I _F = 20 mA, I _C = 4 mA)		4N38				1.0		

TRANSFER CHARACTERISTICS

Characteristic	Test Conditions	Symbol	Device	Min	Typ**	Max	Unit
SWITCHING TIMES Non-Saturated Turn-on Time Turn-off Time	$(V_{CE} = 10 \text{ V}, I_{CE} = 2 \text{ mA})$	t_{on}	ALL		5		μs
	$(R_L = 100 \Omega)$	t_{off}	ALL		5		

ISOLATION CHARACTERISTICS

Characteristic	Test Conditions	Symbol	Device	Min	Typ**	Max	Unit
Isolation Voltage	$(I_{I-O} \leq 1 \mu\text{A}, 1 \text{ min.})$	V_{ISO}	ALL	5300			$(V_{AC(RMS)})$
				7500			$(V_{AC(PEAK)})$
Isolation Resistance	$(V_{I-O} = 500 \text{ VDC})$	R_{ISO}	ALL	10^{11}			Ω
Isolation Capacitance	$(f = 1 \text{ MHz})$	C_{ISO}	ALL		0.5		pF

Notes

* Parameters meet or exceed JEDEC registered data (for 4N38 only)

** All typical values at $T_A = 25^\circ\text{C}$

Fig.1 LED Forward Voltage vs. Forward Current

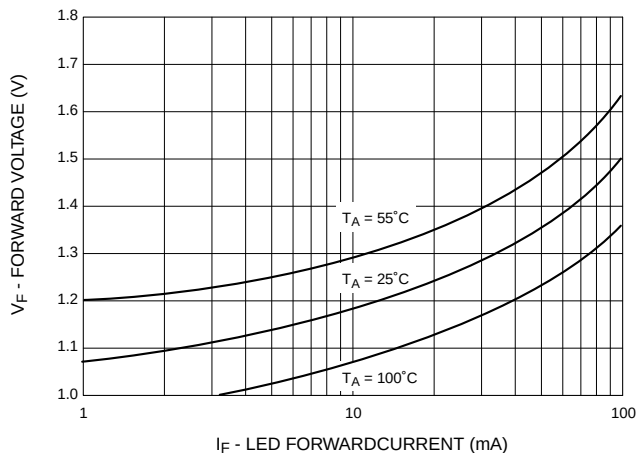
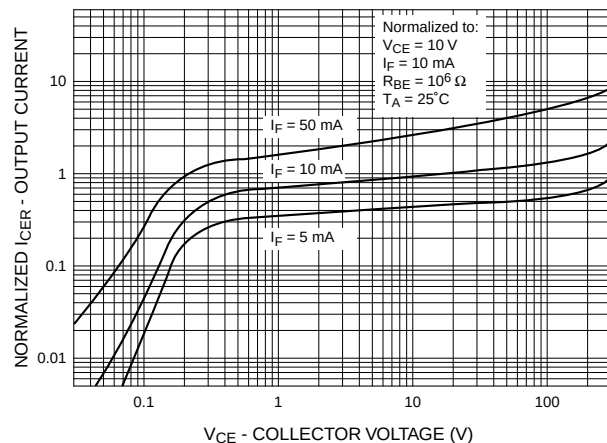


Fig.2 Normalized Output Characteristics



H11D1, H11D2, H11D3, H11D4, 4N38

Fig.3 Normalized Output Current vs. LED Input Current

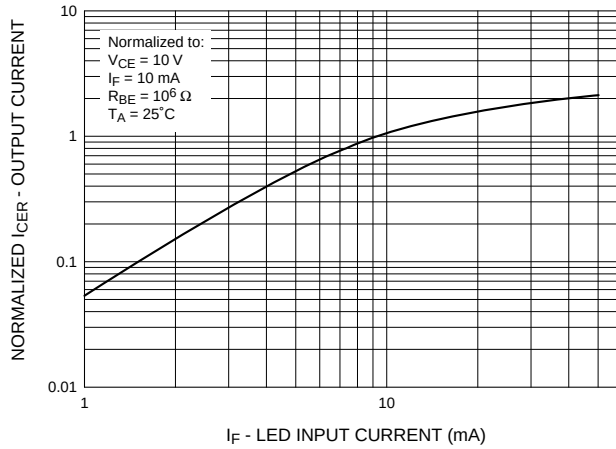


Fig.4 Normalized Output Current vs. Temperature

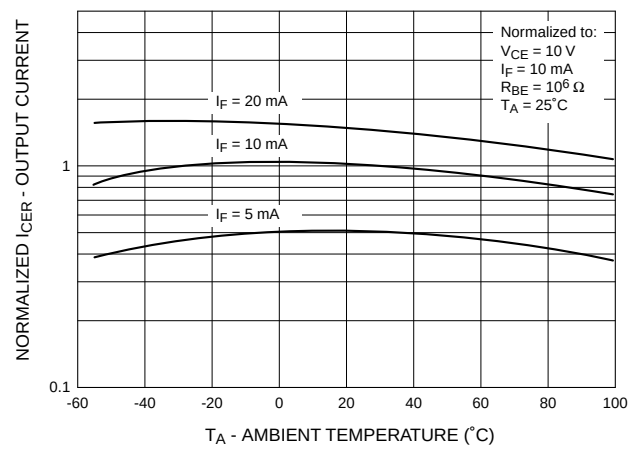
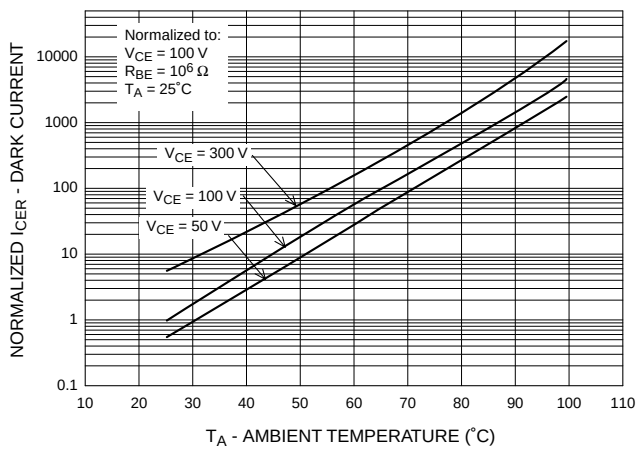
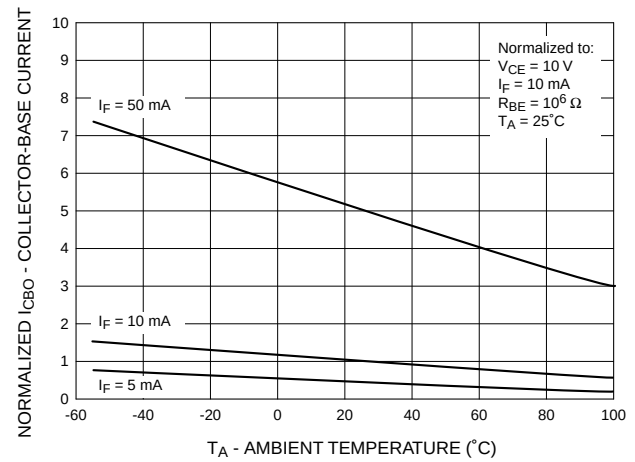


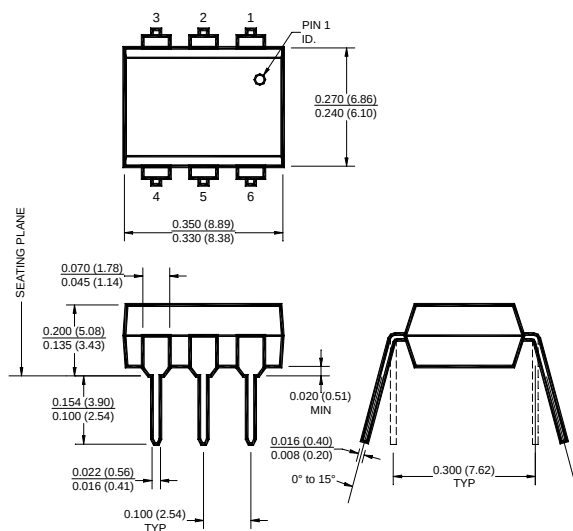
Fig.5 Normalized Dark Current vs. Ambient Temperature



Normalized Collector-Base Current vs. Temperature



Package Dimensions (Through Hole)



The drawing illustrates the mechanical specifications of a 6-pin connector. It includes three views: a top view, a side view, and a cross-sectional view.

Top View Dimensions:

- Overall width: $0.350 (8.89)$ inches / $0.330 (8.38)$ inches
- Pin pitch (center-to-center): $0.070 (1.78)$ inches / $0.045 (1.14)$ inches
- Pin 1 ID. (Internal Diameter) is indicated.
- Pin 1 ID. dimension: $0.270 (6.86)$ inches / $0.240 (6.10)$ inches

Side View Dimensions:

- Overall height: $0.200 (5.08)$ inches / $0.165 (4.18)$ inches
- Lead height: $0.022 (0.56)$ inches / $0.016 (0.41)$ inches
- Lead width: $0.020 (0.51)$ MIN
- Lead pitch: $0.100 (2.54)$ TYP

Cross-sectional View Dimensions:

- Overall width: $0.300 (7.62)$ TYP
- Lead height: $0.016 (0.41)$ MIN
- Lead width: $0.016 (0.40)$ MIN
- Lead pitch: $0.315 (8.00)$ MIN
- Lead width: $0.405 (10.30)$ MAX
- Lead height: $0.016 (0.41)$ / $0.008 (0.20)$

Lead Coplanarity: $0.004 (0.10)$ MAX

The drawing illustrates the mechanical specifications of a 6-pin connector. The top view shows a rectangular body with six pins, numbered 1 through 6. Pin 1 is identified as the ID (Identification) pin. The overall width is 0.350 (8.89) inches, and the overall height is 0.270 (6.86) inches. The distance between the centers of the pins is 0.330 (8.38) inches. The side view shows the profile of the connector, with a maximum height of 0.070 (1.78) inches and a minimum height of 0.045 (1.14) inches. The distance from the seating plane to the top of the pins is 0.200 (5.08) inches. The distance from the seating plane to the bottom of the pins is 0.135 (3.43) inches. The distance from the seating plane to the top of the pins is 0.154 (3.90) inches. The distance from the seating plane to the bottom of the pins is 0.100 (2.54) inches. The distance from the seating plane to the top of the pins is 0.022 (0.56) inches. The distance from the seating plane to the bottom of the pins is 0.016 (0.41) inches. The distance between the centers of the pins is 0.100 (2.54) TYP. The detail view shows the pins with a length of 0.016 (0.40) inches and a diameter of 0.008 (0.20) inches. The pins are angled at 0° to 15° and have a length of 0.400 (10.16) TYP.

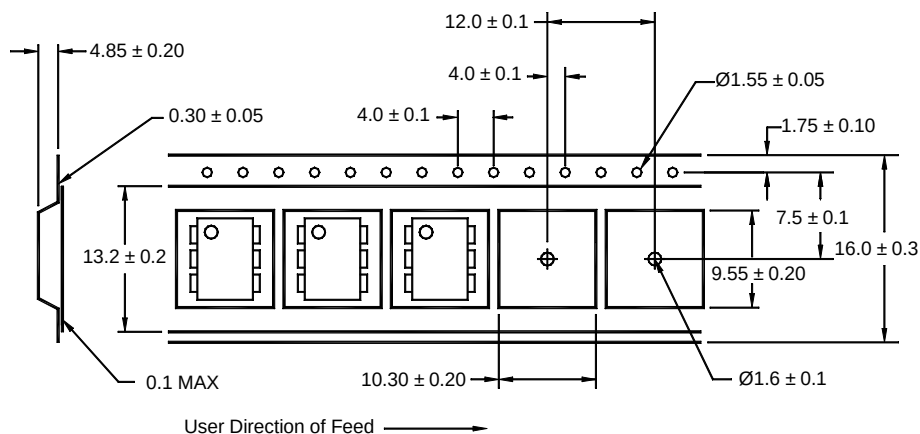
All dimensions are in inches (millimeters)

H11D1, H11D2, H11D3, H11D4, 4N38

ORDERING INFORMATION

Option	Order Entry Identifier	Description
S	.S	Surface Mount Lead Bend
SD	.SD	Surface Mount; Tape and reel
W	.W	0.4" Lead Spacing
300	.300	VDE 0884
300W	.300W	VDE 0884, 0.4" Lead Spacing
3S	.3S	VDE 0884, Surface Mount
3SD	.3SD	VDE 0884, Surface Mount, Tape & Reel

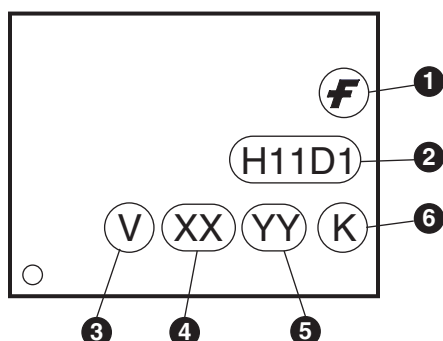
QT Carrier Tape Specifications ("D" Taping Orientation)



NOTE

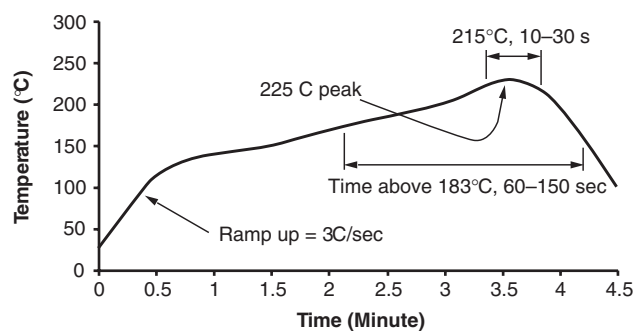
All dimensions are in millimeters

MARKING INFORMATION



Definitions	
1	Fairchild logo
2	Device number
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)
4	Two digit year code, e.g., '03'
5	Two digit work week ranging from '01' to '53'
6	Assembly package code

Reflow Profile (Black Package, No Suffix)



- Peak reflow temperature: 225°C (package surface temperature)
- Time of temperature higher than 183°C for 60–150 seconds
- One time soldering reflow is recommended

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CoolFET™	FRFET™	MicroFET™	PowerTrench®	SuperSOT™-6
CROSSVOLT™	GlobalOptoisolator™	MicroPak™	QFET®	SuperSOT™-8
DOMETM	GTO™	MICROWIRE™	QST™	SyncFET™
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E ² CMOS™	IC™	MSXPro™	Quiet Series™	TINYOPTO™
EnSigna™	i-Lo™	OCX™	RapidConfigure™	TruTranslation™
FACT™	ImpliedDisconnect™	OCXPro™	RapidConnect™	UHC™
FACT Quiet Series™		OPTOLOGIC®	µSerDes™	UltraFET®
Across the board. Around the world.™		OPTOPLANAR™	SILENT SWITCHER®	VCX™
The Power Franchise®		PACMAN™	SMART START™	
Programmable Active Droop™		POP™	SPM™	

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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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