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89H32T8G2

32-lane, 8-port Gen2 PCIe I/O Expansion Switch

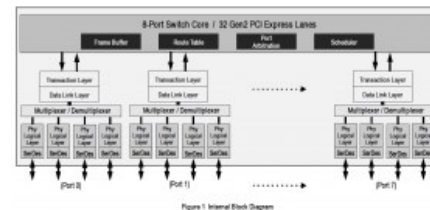
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Features

- 32 PCIe Gen2 Lanes, 8 Switch Ports
- Switch architecture optimized for I/O Expansion
- Supports root-to-peripheral (upstream-to-downstream) and peer-to-peer (downstream-to-downstream) data transfers
- Internal buffering and flow control credits optimized for maximum bandwidth and large payloads
- Low latency, Low power



Product Specification

PCIe Lanes (#)	Ports (#)	PCIe Generation	Pkg. Code	Pkg. Type	Length (mm)	Width (mm)	Temp. Grade
32	8	Gen2	BLG484, BL484	FCBGA	23	23	I, C

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Carrier Type	Sample & Buy
89H32T8G2ZCBL	Active	BL484	FCBGA	484	C	No	Tray	Check Availability ▶
89H32T8G2ZCBL8	Active	BL484	FCBGA	484	C	No	Reel	Check Availability ▶
89H32T8G2ZCBLG	Active	BLG484	FCBGA	484	C	Yes	Tray	Check Availability ▶
89H32T8G2ZCBLG8	Active	BLG484	FCBGA	484	C	Yes	Reel	Check Availability ▶
89H32T8G2ZCBLGI	Active	BLG484	FCBGA	484	I	Yes	Tray	Check Availability ▶
89H32T8G2ZCBLGI8	Active	BLG484	FCBGA	484	I	Yes	Reel	Check Availability ▶
89H32T8G2ZCBLI	Active	BL484	FCBGA	484	I	No	Tray	Check Availability ▶
89H32T8G2ZCBLI8	Active	BL484	FCBGA	484	I	No	Reel	Check Availability ▶

Technical Documentation

	Title	Other Languages	Type	Format	File Size	Date ▼
Datasheets & Errata						
	89PES32T8G2 Data Sheet		Datasheet	PDF	296 KB	Nov 28, 2011
	89PES32T8G2 Device Errata (Revision ZC)		Errata	PDF	170 KB	Feb 11, 2011
	89PES32T8G2 Device Errata (Revision ZB)		Errata	PDF	46 KB	Jun 26, 2009
	+ show all (4)					

