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89H48H12AG2
48-lane, 12-port PCIe Gen2 System Interconnect Switch

Provides 48 PCIe Gen2 lanes and 12 ports of high-performance, deterministic system interconnect switching. All ports receive full line rate, non-blocking throughput for multiple traffic flows regardless of switch loading.

Features

- 48 PCIe Gen2 Lanes, 12 Switch Ports
- Non-blocking switch architecture optimized for High Performance System Interconnect
- Partionable switch architecture for true mutli-root support
- Internal buffering and flow control credits optimized for maximum bandwidth and large payloads
- Low latency, Low power

Product Specification

PCIE LANES (#)	PORTS (#)	PCIE GENERATION	PKG. CODE	PKG. TYPE	LENGTH (MM)	WIDTH (MM)	TEMP. GRADE
48	12	Gen2	BL1156, BLG1156	FCBGA	35	35	I, C

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
89H48H12AG2ZCBL	Active	BL1156	FCBGA	1156	C	N	Check Inventory
89H48H12AG2ZCBLG	Active	BLG1156	FCBGA	1156	C	Y	Check Inventory
89H48H12AG2ZCBLGI	Active	BLG1156	FCBGA	1156	I	Y	Check Inventory
89H48H12AG2ZCBLI	Active	BL1156	FCBGA	1156	I	N	Check Inventory

Documents

	TITLE	TYPE	FORMAT	FILE SIZE	DATE
	AN-721, IDT PCIe Gen2 Switch Hardware Design Guide	Application Note	PDF	341 KB	08/14/2009
	AN-720, Enabling Hot-Plug in IDT PCIe Gen2 SI Switches	Application Note	PDF	182 KB	07/20/2009
	AN-715, Multi-Port SSC Support in IDT PCIe Gen2 Switches	Application Note	PDF	312 KB	10/26/2009
	PES48H12AG2 Data Sheet (myIDT Access Required. Click for details.)	Datasheet	PDF	124 KB	01/21/2010
	IDT PCI Express Solutions Overview	Overview	PDF	2.47 MB	09/14/2011

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