

TSM240N03CX

30V N-Channel Power MOSFET

SOT-23



Pin Definition:

1. Gate
2. Source
3. Drain

Key Parameter Performance

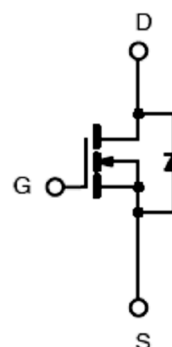
Parameter	Value	Unit
V_{DS}	30	V
$R_{DS(on)}$ (max)	$V_{GS} = 10V$	24
	$V_{GS} = 4.5V$	34
Q_g	4.1	nC

Ordering Information

Part No.	Package	Packing
TSM240N03CX RFG	SOT-23	3kpcs / 7+Reel

Note: %G+denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	$T_C = 25^\circ\text{C}$	6.5
		$T_C = 100^\circ\text{C}$	4.1
Pulsed Drain Current ^(Note 1)	I_{DM}	26	A
Single Pulse Avalanche Energy ^(Note 2)	E_{AS}	32	mJ
Power Dissipation @ $T_C = 25^\circ\text{C}$	P_D	1.56	W
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 to +150	$^\circ\text{C}$

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Ambient	R_{JA}	80	$^\circ\text{C/W}$

Electrical Specifications (T_C = 25°C unless otherwise noted)

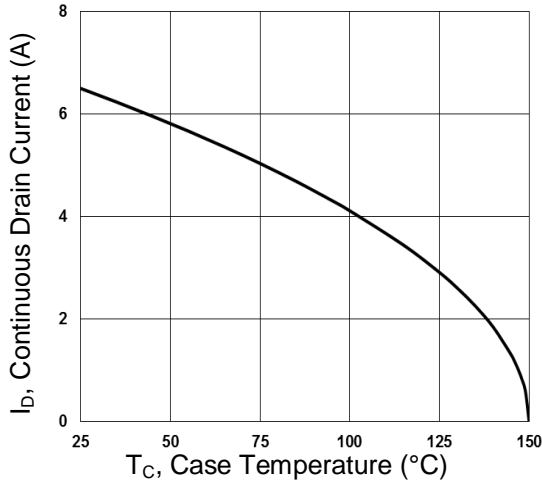
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	BV _{DSS}	30	--	--	V
Drain-Source On-State Resistance	V _{GS} = 10V, I _D = 6A	R _{DS(on)}	--	17	24	m
	V _{GS} = 4.5V, I _D = 4A		--	22	34	
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	V _{GS(TH)}	1.2	1.4	2.5	V
Zero Gate Voltage Drain Current	V _{DS} = 30V, V _{GS} = 0V	I _{DSS}	--	--	1	μA
	V _{DS} = 24V, T _J = 125°C		--	--	10	
Gate Body Leakage	V _{GS} = ±20V, V _{DS} = 0V	I _{GSS}	--	--	±100	μA
Forward Transconductance ^(Note 3)	V _{DS} = 10V, I _D = 4A	g _{fs}	--	6.5	--	S
Dynamic						
Total Gate Charge ^(Note 3,4)	V _{DS} = 15V, I _D = 6A, V _{GS} = 4.5V	Q _g	--	4.1	--	nC
Gate-Source Charge ^(Note 3,4)		Q _{gs}	--	1	--	
Gate-Drain Charge ^(Note 3,4)		Q _{gd}	--	2.1	--	
Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	C _{iss}	--	345	--	pF
Output Capacitance		C _{oss}	--	55	--	
Reverse Transfer Capacitance		C _{rss}	--	32	--	
Switching						
Turn-On Delay Time ^(Note 3,4)	V _{DD} = 15V, I _D = 1A, V _{GS} = 10V, R _G = 6	t _{d(on)}	--	2.8	--	ns
Turn-On Rise Time ^(Note 3,4)		t _r	--	7.2	--	
Turn-Off Delay Time ^(Note 3,4)		t _{d(off)}	--	15.8	--	
Turn-Off Fall Time ^(Note 3,4)		t _f	--	4.6	--	
Source-Drain Diode Ratings and Characteristic						
Maximum Continuous Drain-Source Diode Forward Current	Integral reverse diode in the MOSFET	I _S	--	--	6.5	A
Maximum Pulse Drain-Source Diode Forward Current		I _{SM}	--	--	26	A
Diode-Source Forward Voltage	V _{GS} = 0V, I _S = 1A	V _{SD}	--	--	1	V

Note:

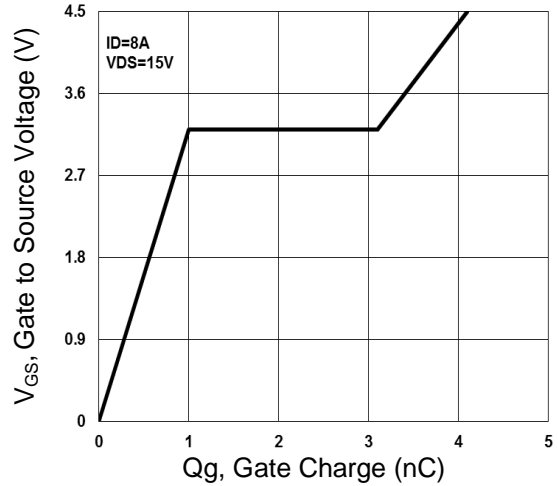
1. Pulse width limited by safe operating area
2. L = 1mH, I_{AS} = 8A, V_{DD} = 25V, R_G = 25 Ω, Starting T_J = 25°C
3. Pulse test: pulse width m300μs, duty cycle m2%
4. Switching time is essentially independent of operating temperature.

Electrical Characteristics Curve

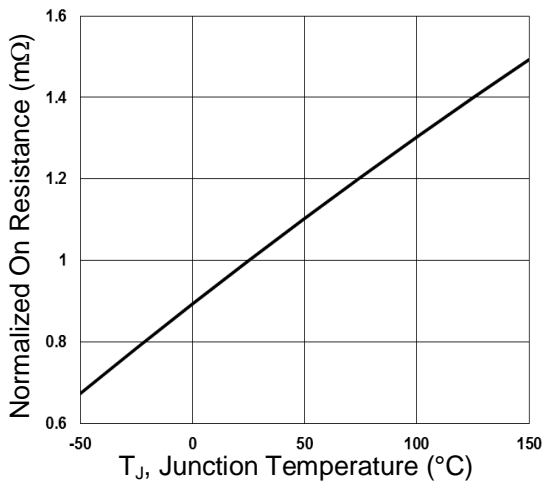
Continuous Drain Current vs. T_C



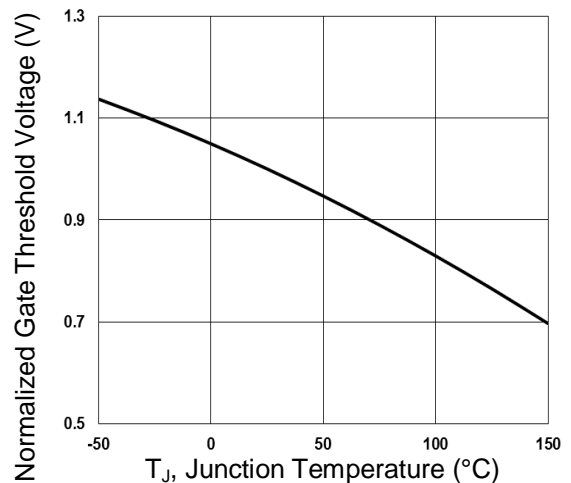
Gate Charge



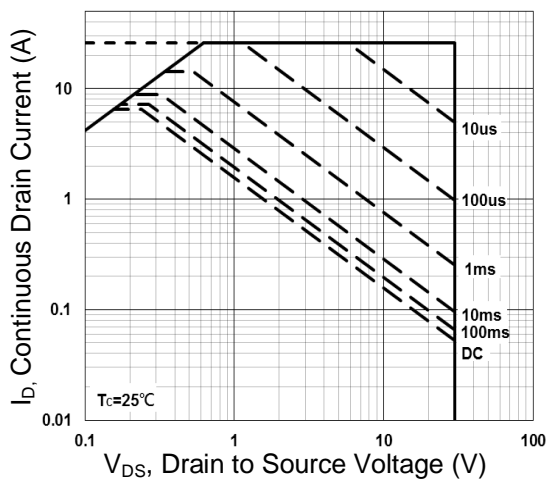
On-Resistance vs. Junction Temperature



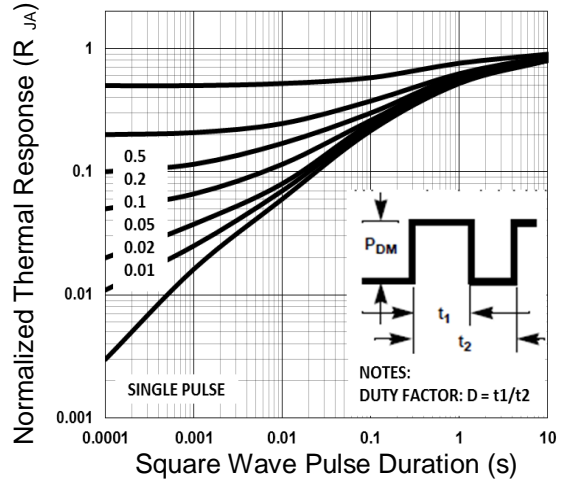
Threshold Voltage vs. Junction Temperature



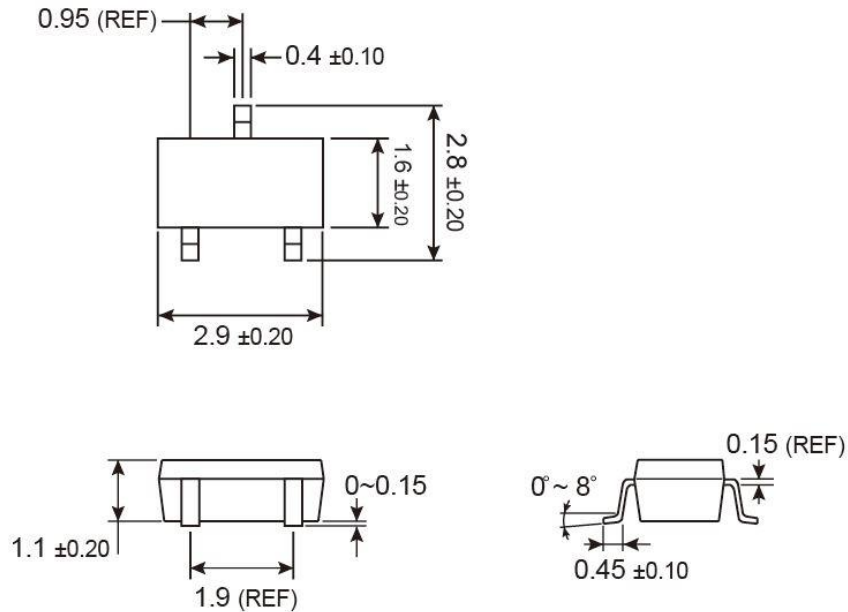
Maximum Safe Operating Area



Normalized Thermal Transient Impedance Curve

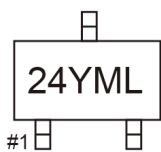


SOT-23 Mechanical Drawing



Unit: Millimeters

Marking Diagram



- 24** = Device Code
- Y** = Year Code
- M** = Month Code for Halogen Free Product
(**O**=Jan, **P**=Feb, **Q**=Mar, **R**=Apr, **S**=May, **T**=Jun, **U**=Jul, **V**=Aug, **W**=Sep, **X**=Oct, **Y**=Nov, **Z**=Dec)
- L** = Lot Code

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