

TC74HC4066AP, TC74HC4066AF, TC74HC4066AFN, TC74HC4066AFT

QUAD BILATERAL SWITCH

The TC74HC4066A is a high speed CMOS QUAD BILATERAL SWITCH fabricated with silicon gate C²MOS technology.

It consists of four independent high speed switches capable of controlling either digital or analog signals while maintaining the CMOS low power dissipation.

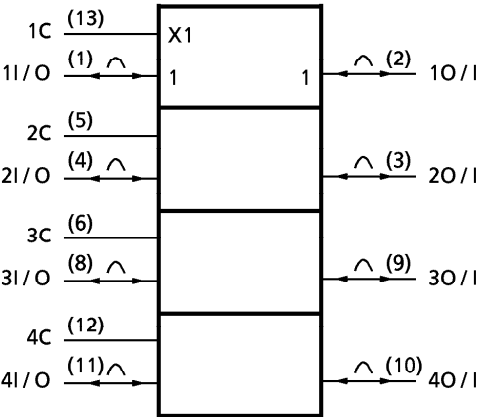
Control input (C) is provided to control the switch. The switch turns ON while the C input is high, and the switch turns OFF while low.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

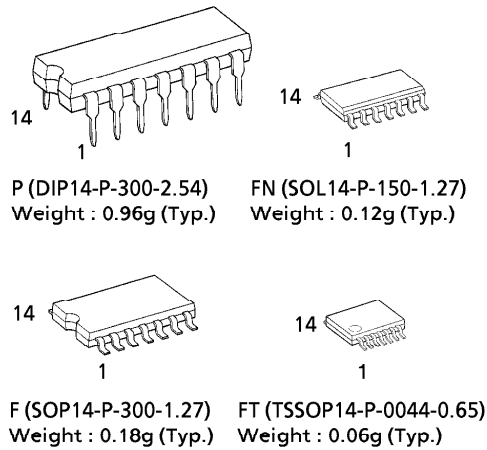
FEATURES :

- High Speed $t_{pd} = 7\text{ns}(\text{typ.})$ at $V_{CC} = 5\text{V}$
- Low Power Dissipation $I_{CC} = 1\mu\text{A}(\text{Max.})$ at $T_a = 25^{\circ}\text{C}$
- High Noise Immunity $V_{NIH} = V_{NIL} = 28\% V_{CC} (\text{Min.})$
- Low ON Resistance $R_{ON} = 50\Omega(\text{typ.})$ at $V_{CC} = 9\text{V}$
- High Degree of Linearity $\text{THD} = 0.05\%(\text{typ.})$ at $V_{CC} = 5\text{V}$
- Pin and Function Compatible with 4066B

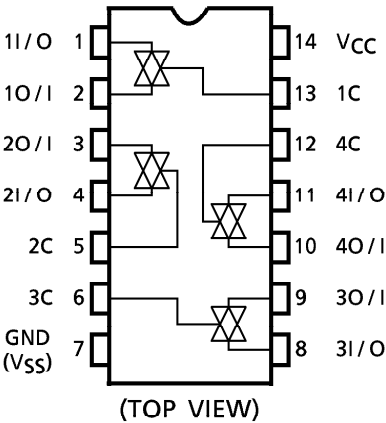
IEC LOGIC SYMBOL



(Note) The JEDEC SOP (FN) is not available in Japan.



PIN ASSIGNMENT



TRUTH TABLE

CONTROL	SWITCH FUNCTION
H	ON
L	OFF

● TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	V_{CC}	$-0.5 \sim 13$	V
Control Input Voltage	V_{IN}	$-0.5 \sim V_{CC} + 0.5$	V
Switch I/O Voltage	$V_{I/O}$	$-0.5 \sim V_{CC} + 0.5$	V
Control Input Diode Current	I_{CK}	± 20	mA
I/O Diode Current	$I_{I/O}$	± 20	mA
Switch through Current	I_T	± 25	mA
DC V_{CC} /Ground Current	I_{CC}	± 50	mA
Power Dissipation	P_D	500 (DIP)* / 180 (SOP / TSSOP)	mW
Storage Temperature	T_{stg}	$-65 \sim 150$	$^{\circ}\text{C}$

*500mW in the range of $T_a = -40^{\circ}\text{C} \sim 65^{\circ}\text{C}$. From $T_a = 65^{\circ}\text{C}$ to 85°C a derating factor of $-10\text{mW}/^{\circ}\text{C}$ shall be applied until 300mW.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V_{CC}	$2 \sim 12$	V
Control Input Voltage	V_{IN}	$0 \sim V_{CC}$	V
Switch I/O Voltage	$V_{I/O}$	$0 \sim V_{CC}$	V
Operating Temperature	T_{opr}	$-40 \sim 85$	$^{\circ}\text{C}$
Input Rise and Fall Time	t_r, t_f	$0 \sim 1000 (V_{CC} = 2.0\text{V})$ $0 \sim 500 (V_{CC} = 4.5\text{V})$ $0 \sim 400 (V_{CC} = 6.0\text{V})$ $0 \sim 250 (V_{CC} = 10.0\text{V})$	ns

DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION	V_{CC} (V)	$T_a = 25^{\circ}\text{C}$			$T_a = -40 \sim 85^{\circ}\text{C}$		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
High - Level Control Input Voltage	V_{IHC}		2.0 4.5 9.0 12.0	1.50 3.15 6.30 8.40	— — — —	— — — —	1.50 3.15 6.30 8.40	— — — —	V
Low - Level Control Input Voltage	V_{ILC}		2.0 4.5 9.0 12.0	— — — —	— — — —	0.50 1.35 2.70 3.60	— — — —	0.50 1.35 2.70 3.60	
ON Resistance	R_{ON}	$V_{IN} = V_{IHC}$	4.5	—	96	170	—	200	Ω
		$V_{I/O} = V_{CC}$ to GND	9.0	—	55	85	—	100	
		$V_{I/O} \leq 1\text{mA}$	12.0	—	45	80	—	90	
		$V_{IN} = V_{IHC}$	2.0	—	160	—	—	—	
		$V_{I/O} = V_{CC}$ to GND	4.5	—	70	100	—	130	
		$V_{I/O} \leq 1\text{mA}$	9.0	—	50	75	—	95	
			12.0	—	45	70	—	90	
Difference of ON Resistance Between Switches	ΔR_{ON}	$V_{IN} = V_{IHC}$	4.5	—	10	—	—	—	
		$V_{I/O} = V_{CC}$ to GND	9.0	—	5	—	—	—	
		$V_{I/O} \leq 1\text{mA}$	12.0	—	5	—	—	—	
Input / Output Leakage Current (SWITCH OFF)	I_{OFF}	$V_{OS} = V_{CC}$ or GND $V_{IS} = \text{GND}$ or V_{CC} $V_{IN} = V_{ILC}$	12.0	—	—	± 100	—	± 1000	nA
Switch Input Leakage Current (SWITCH ON, OUTPUT OPEN)	I_{IZ}	$V_{OS} = V_{CC}$ or GND $V_{INH} = V_{IHC}$	12.0	—	—	± 100	—	± 1000	
Control Input Current	I_{IN}	$V_{IN} = V_{CC}$ or GND	12.0	—	—	± 100	—	± 1000	
Quiescent Supply Current	I_{OC}	$V_{IN} = V_{CC}$ or GND	6.0	—	—	1.0	—	10.0	μA
			9.0	—	—	4.0	—	40.0	
			12.0	—	—	8.0	—	80.0	

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AC ELECTRICAL CHARACTERISTICS ($C_L = 50\text{pF}$, Input $t_r = t_f = 6\text{ns}$)

PARAMETER	SYMBOL	TEST CONDITION	$V_{CC}(\text{V})$	$T_a = 25^\circ\text{C}$			$T_a = -40 \sim 85^\circ\text{C}$		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
Phase difference between Input and Output	ϕ_{I-O}		2.0 4.5 9.0 12.0	— — — —	10 4 3 3	50 10 8 7	— — — —	65 13 10 9	pF
Output Enable Time	t_{pZL} t_{pZH}	$R_L = 1\text{K}\Omega$	2.0 4.5 9.0 12.0	— — — —	18 8 6 6	100 20 12 12	— — — —	125 25 22 18	
Output Disable Time	t_{pLZ} t_{pHZ}	$R_L = 1\text{K}\Omega$	2.0 4.5 9.0 12.0	— — — —	20 10 8 8	115 23 20 18	— — — —	145 29 25 22	
Maximum Control Input Frequency		$R_L = 1\text{K}\Omega$ $C_L = 15\text{pF}$ $V_{OUT} = 1/2 V_{CC}$	2.0 4.5 9.0 12.0	— — — —	30 30 30 30	— — — —	— — — —	— — — —	
Control Input Capacitance	C_{IN}			—	5	10	—	10	
Switch Terminal Capacitance	$C_{I/O}$			—	6	—	—	—	
Feed through Capacitance	C_{IOS}			—	0.5	—	—	—	pF
Power Dissipation Capacitance	C_{PD}	(1)		—	15	—	—	—	

Note (1) C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation :

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 4 \text{ (per channel)}$$

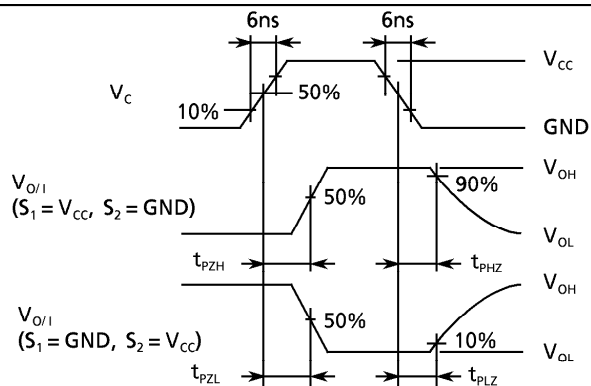
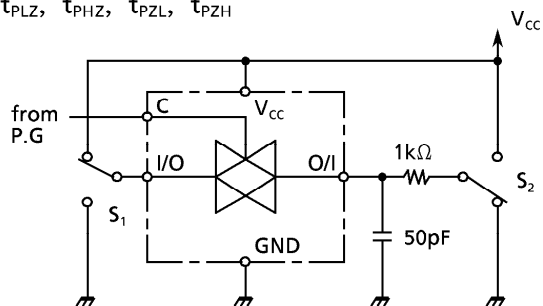
ANALOG SWITCH CHARACTERISTICS ($GND = 0\text{V}$, $T_a = 25^\circ\text{C}$)

PARAMETER	SYMBOL	TEST CONDITION	$V_{CC}(\text{V})$	TYP.	UNIT
Sine Wave Distortion (T. H. D)		$f_{IN} = 1\text{kHz}$ $R_L = 10\text{k}\Omega$ $C_L = 50\text{pF}$ $V_{IN} = 4.0\text{V}_{P-P}$ $V_{IN} = 8.0\text{V}_{P-P}$ @ $V_{CC} = 4.5\text{V}$ @ $V_{CC} = 9.0\text{V}$	4.5 9.0	0.05 0.04	%
Frequency Response (Switch ON)	f_{MAX}	Adjust f_{IN} voltage to obtain 0dBm at V_{OS} Increase f_{IN} Frequency until dB Meter reads -3dB $R_L = 50\Omega$, $C_L = 10\text{pF}$ $f_{IN} = 1\text{kHz}$, Sine Wave	4.5 9.0	200 200	MHz
Feedthrough Attenuation (Switch OFF)		V_{IN} is centered at $V_{CC}/2$ Adjust input for 0dBm $R_L = 600\Omega$, $C_L = 50\text{pF}$ $f_{IN} = 1\text{kHz}$, Sine Wave	4.5 9.0	-60 -60	dB
Crosstalk (Control Input to Signal Output)		$R_L = 600\Omega$, $C_L = 50\text{pF}$ $f_{IN} = 1\text{kHz}$, Square Wave ($t_r = t_f = 6\text{ns}$)	4.5 9.0	60 100	mV
Crosstalk (Between any switches)		Adjust V_{IN} to obtain 0dBm at Input $R_L = 600\Omega$, $C_L = 50\text{pF}$ $f_{IN} = 1\text{kHz}$, Sine Wave	4.5 9.0	-60 -60	dB

Note : These characteristics are determined by design of devices.

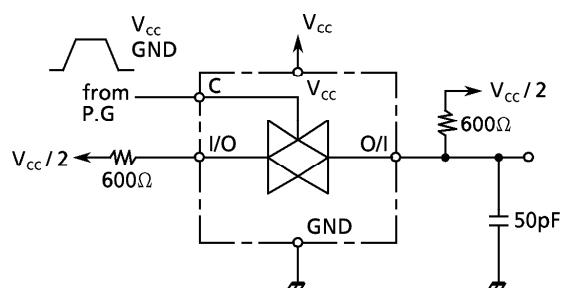
SWITCHING CHARACTERISTICS TEST CIRCUITS

1. t_{PLZ} , t_{PHZ} , t_{PZL} , t_{PZH}

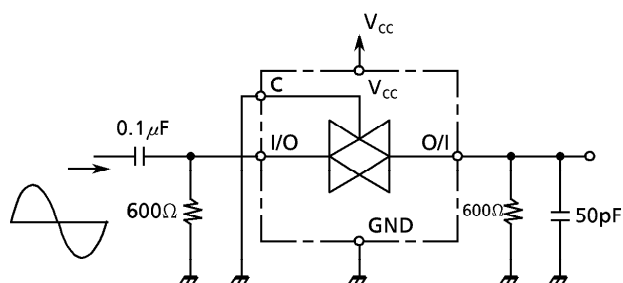


2. CROSS TALK (CONTROL INPUT-SWITCH OUTPUT)

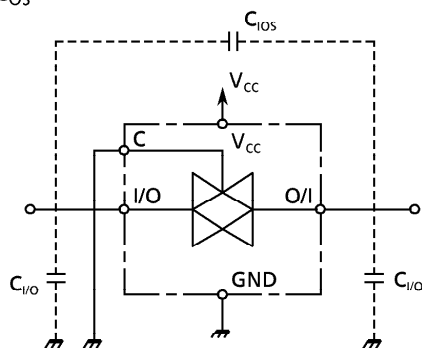
$f_{in} = 1MHz$ $duty = 50\%$ $tr = tf = 6ns$



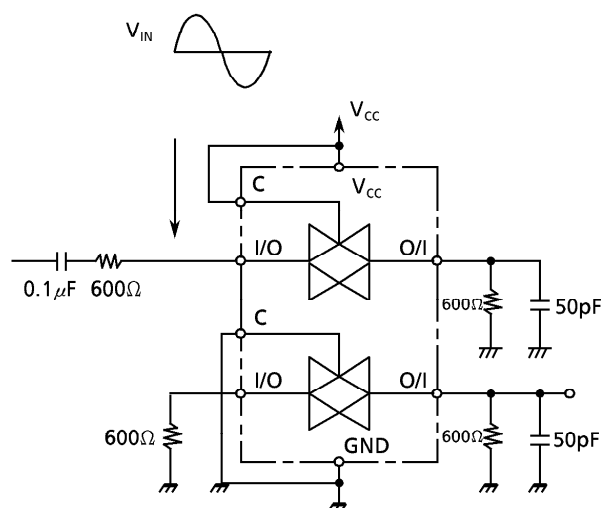
3. FEEDTHROUGH ATTENUATION



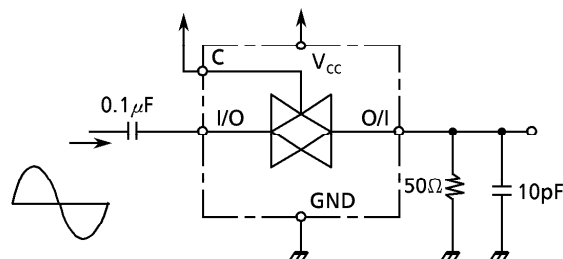
4. C_{IOS} , C_{IS} , C_{OS}



5. CROSSTALK (BETWEEN ANY TWO SWITCHES)

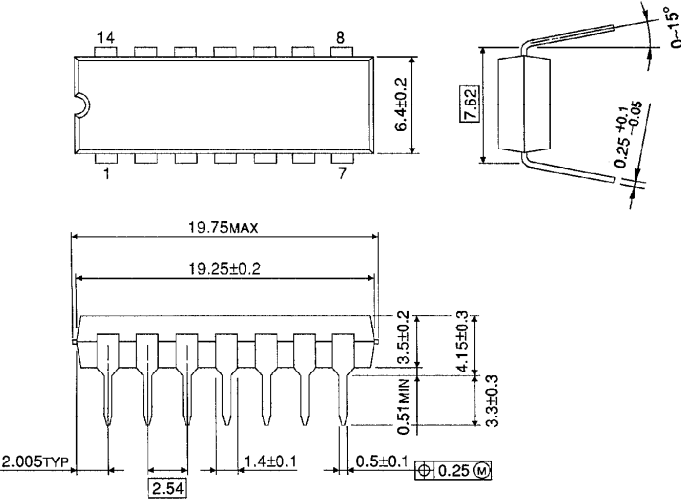


6. FREQUENCY RESPONSE (SWITCH ON)



DIP 14PIN OUTLINE DRAWING (DIP14-P-300-2.54)

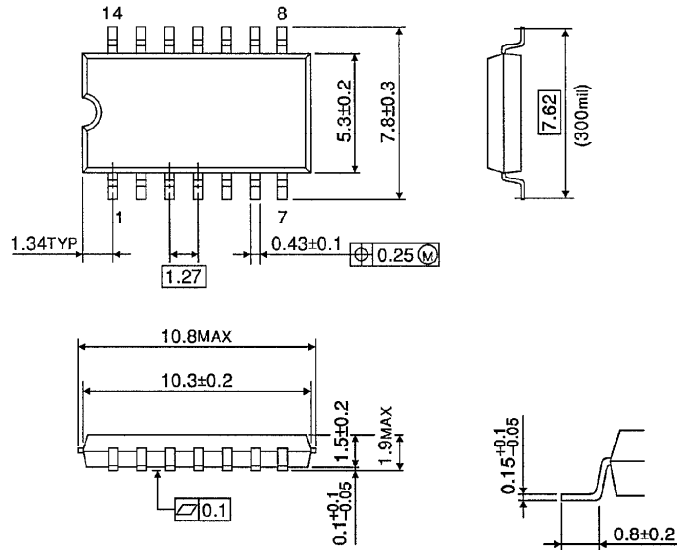
Unit in mm



Weight : 0.96g (Typ.)

SOP 14PIN (200mil BODY) OUTLINE DRAWING (SOP14-P-300-1.27)

Unit in mm

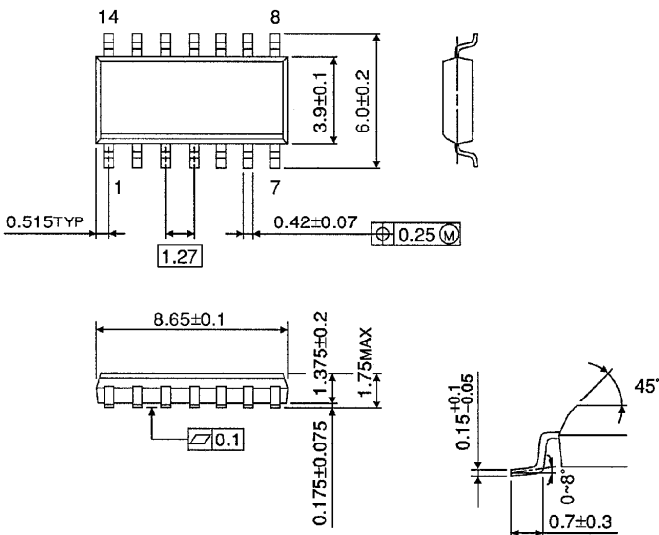


Weight : 0.18g (Typ.)

SOP 14PIN (150mil BODY) OUTLINE DRAWING (SOL14-P-150 -1.27)

Unit in mm

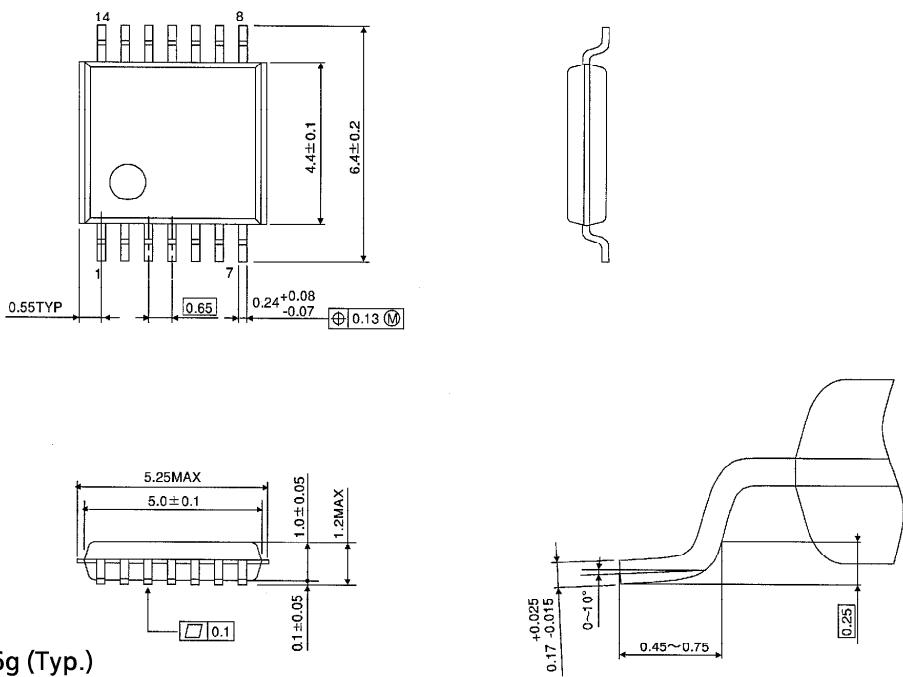
(Note) This package is not available in Japan.



Weight : 0.12g (Typ.)

TSSOP 14PIN (170mil BODY) OUTLINE DRAWING (TSSOP14-P-0044-0.65)

Unit in mm



Weight : 0.06g (Typ.)