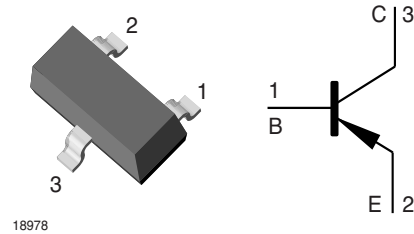


Small Signal Transistor (PNP)

Features

- PNP Silicon Epitaxial Planar Transistor for switching and amplifier applications.
- As complementary type, the NPN transistor MMBT4401 is recommended.
- This transistor is also available in the TO-92 case with the type designation 2N4403.



Mechanical Data

Case: SOT-23 Plastic case

Weight: approx. 8.8 mg

Packaging Codes/Options:

GS18 / 10 k per 13" reel (8 mm tape), 10 k/box

GS08 / 3 k per 7" reel (8 mm tape), 15 k/box

Parts Table

Part	Type differentiation	Ordering code	Marking	Remarks
MMBT4403	h_{FE} , 100 to 300 @ 150 mA	MMBT4403-GS18 or MMBT4403-GS08	2T	Tape and Reel

Absolute Maximum Ratings

$T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Collector - base voltage		$-V_{CBO}$	40	V
Collector - emitter voltage		$-V_{CEO}$	40	V
Emitter - base voltage		$-V_{EBO}$	5	V
Collector current		$-I_C$	600	mA
Power dissipation ¹⁾	$T_A = 25\text{ }^{\circ}\text{C}$	P_{tot}	225	mW
	Derate above 25 $^{\circ}\text{C}$	P_{tot}	1.8	mW/ $^{\circ}\text{C}$
Power dissipation ²⁾	$T_A = 25\text{ }^{\circ}\text{C}$	P_{tot}	300	mW
	Derate above 25 $^{\circ}\text{C}$	P_{tot}	2.4	mW/ $^{\circ}\text{C}$

¹⁾ FR-5 Board = 1.0 x 0.75 x 0.062 in.

²⁾ Alumina Substrate = 0.4 x 0.3 x 0.024 in. 99.5 % alumina

Maximum Thermal Resistance

Parameter	Test condition	Symbol	Value	Unit
Thermal resistance junction to ambient air		R_{thJA}	556 ¹⁾	°C/W
		R_{thJA}	417 ²⁾	°C/W
Junction temperature		T_j	150	°C
Storage temperature range		T_S	- 55 to + 150	°C

¹⁾ FR-5 Board = 1.0 x 0.75 x 0.062 in.

²⁾ Alumina Substrate = 0.4 x 0.3 x 0.024 in. 99.5 % alumina

Electrical DC Characteristics

Parameter	Test condition	Symbol	Min	Typ	Max	Unit
DC current gain	- $V_{CE} = 1\text{ V}$, - $I_C = 0.1\text{ mA}$	h_{FE}	30			
	- $V_{CE} = 1\text{ V}$, - $I_C = 1\text{ mA}$	h_{FE}	60			
	- $V_{CE} = 1\text{ V}$, - $I_C = 10\text{ mA}$	h_{FE}	100			
	- $V_{CE} = 2\text{ V}$, - $I_C = 150\text{ mA}$ ¹⁾	h_{FE}	100		300	
	- $V_{CE} = 2\text{ V}$, - $I_C = 500\text{ mA}$ ¹⁾	h_{FE}	20			
Collector - base breakdown voltage	- $I_C = 0.1\text{ mA}$, $I_E = 0$	- $V_{(BR)CBO}$	40			V
Collector - emitter breakdown voltage ¹⁾	- $I_C = 1\text{ mA}$, $I_B = 0$	- $V_{(BR)CEO}$	40			V
Emitter - base breakdown voltage	- $I_E = 0.1\text{ mA}$, $I_C = 0$	- $V_{(BR)EBO}$	5			V
Collector - emitter saturation voltage ¹⁾	- $I_C = 150\text{ mA}$, - $I_B = 15\text{ mA}$	- V_{CEsat}			0.40	V
	- $I_C = 500\text{ mA}$, - $I_B = 50\text{ mA}$	- V_{CEsat}			0.75	V
Base - emitter saturation voltage ¹⁾	- $I_C = 150\text{ mA}$, - $I_B = 15\text{ mA}$	- V_{BEsat}	0.75		0.95	V
	- $I_C = 500\text{ mA}$, - $I_B = 50\text{ mA}$	- V_{BEsat}			1.30	V
Collector - emitter cut - off current	- $V_{EB} = 0.4\text{ V}$, - $V_{CE} = 35\text{ V}$	- I_{CEV}			100	nA
Emitter - base cut - off current	- $V_{EB} = 0.4\text{ V}$, - $V_{CE} = 35\text{ V}$	- I_{BEV}			100	nA

¹⁾ Pulse test: pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

Electrical AC Characteristics

Parameter	Test condition	Symbol	Min	Typ	Max	Unit
Gain - bandwidth product	- $V_{CE} = 10\text{ V}$, - $I_C = 20\text{ mA}$, $f = 100\text{ MHz}$	f_T	200			MHz
Collector - base capacitance	- $V_{CB} = 10\text{ V}$, $f = 1\text{ MHz}$, $I_E = 0$	C_{CBO}			8.5	pF
Emitter - base capacitance	- $V_{EB} = 0.5\text{ V}$, $f = 1\text{ MHz}$, $I_C = 0$	C_{EBO}			30	pF
Input impedance	- $V_{CE} = 10\text{ V}$, - $I_C = 1\text{ mA}$, $f = 1\text{ kHz}$	h_{ie}	1.5		15	k Ω
Small signal current gain	- $V_{CE} = 10\text{ V}$, - $I_C = 1\text{ mA}$, $f = 1\text{ kHz}$	h_{fe}	60		500	
Voltage feedback ratio	- $V_{CE} = 10\text{ V}$, - $I_C = 1\text{ mA}$, $f = 1\text{ kHz}$	h_{re}	0.1×10^{-4}		8×10^{-4}	
Output admittance	- $V_{CE} = 10\text{ V}$, - $I_C = 1\text{ mA}$, $f = 1\text{ kHz}$	h_{oe}	1		100	μS

Parameter	Test condition	Symbol	Min	Typ	Max	Unit
Delay time (see fig.1)	- $I_{B1} = 15 \text{ mA}$, - $I_C = 150 \text{ mA}$, - $V_{CC} = 30 \text{ V}$, - $V_{EB} = 2 \text{ V}$	t_d			15	ns
Rise time (see fig.1)	- $I_{B1} = 15 \text{ mA}$, - $I_C = 150 \text{ mA}$, - $V_{CC} = 30 \text{ V}$, - $V_{EB} = 2 \text{ V}$	t_r			20	ns
Storage time (see fig.2)	- $I_{B1} = - I_{B2} = 15 \text{ mA}$, - $I_C = 150 \text{ mA}$, - $V_{CC} = 30 \text{ V}$	t_s			225	ns
Fall time (see fig.2)	- $I_{B1} = - I_{B2} = 15 \text{ mA}$, - $I_C = 150 \text{ mA}$, - $V_{CC} = 30 \text{ V}$	t_f			30	ns

Switching Time Equivalent Test Circuit

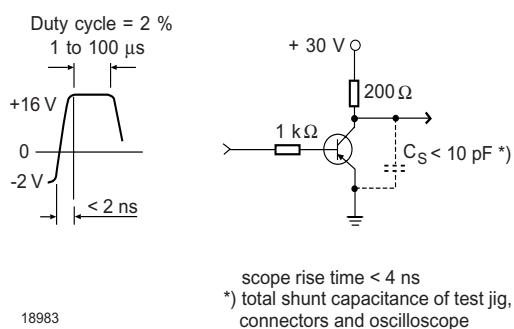


Figure 1. Turn-On Time

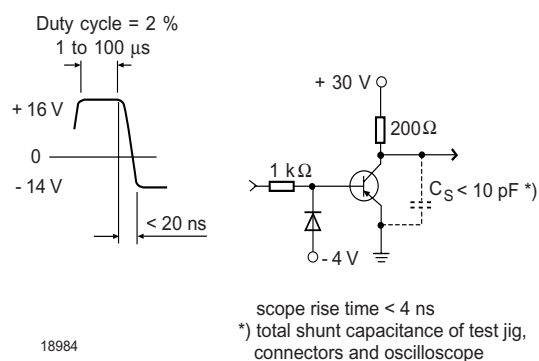
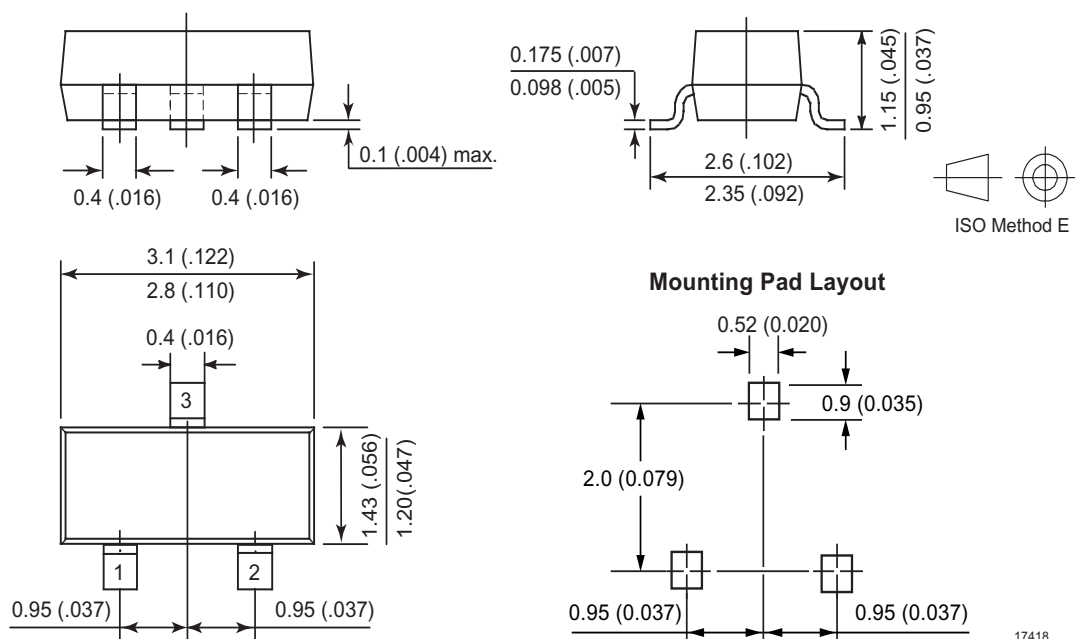


Figure 2. Turn-Off Time

Package Dimensions in mm (Inches)





Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design
and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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